

FIG. 1

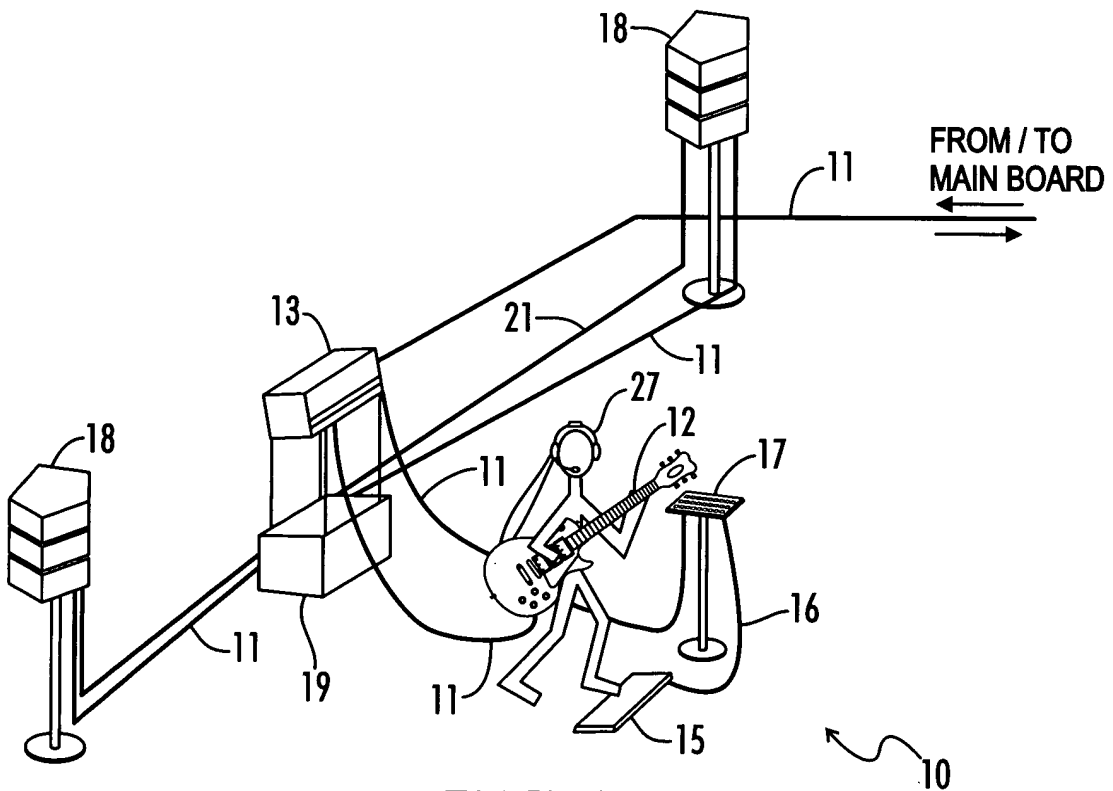


FIG. 2

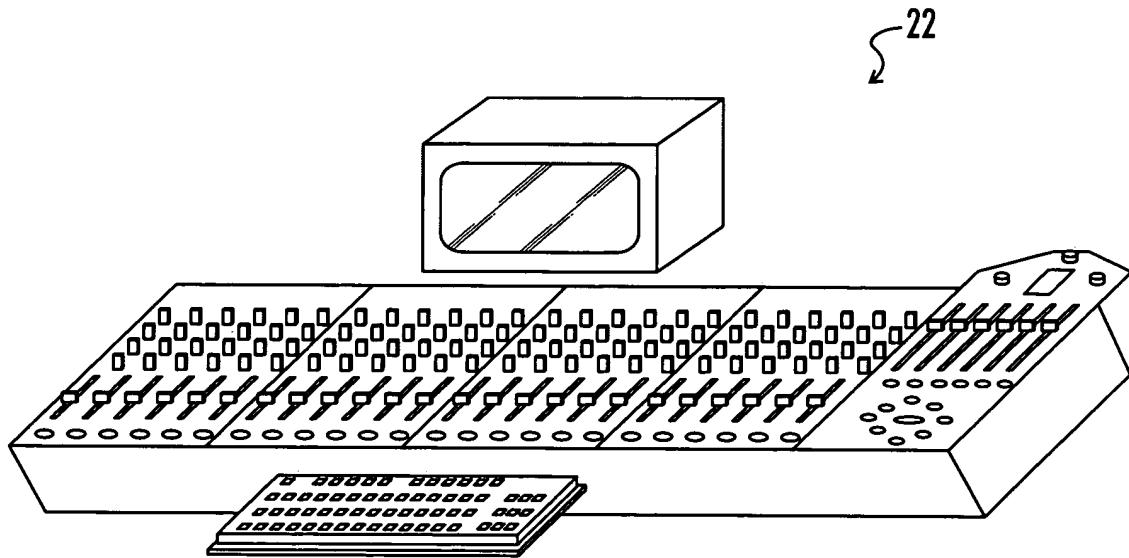


FIG. 3

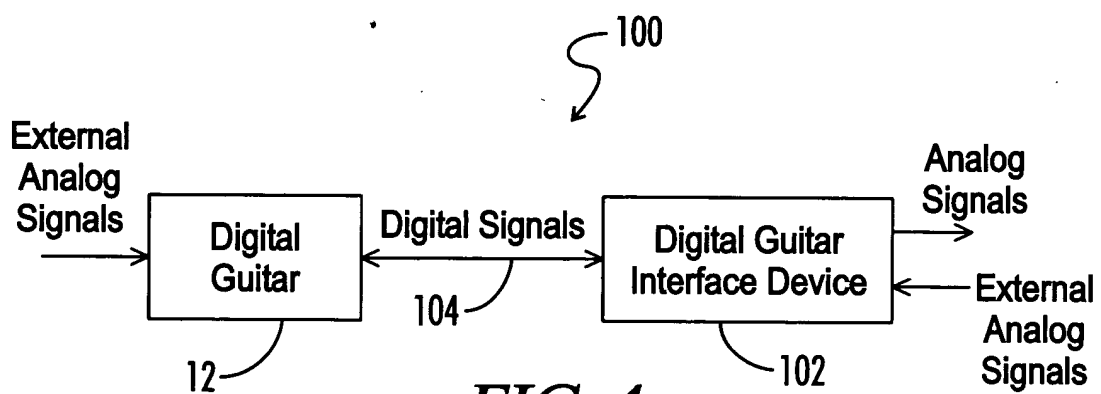


FIG. 4

+

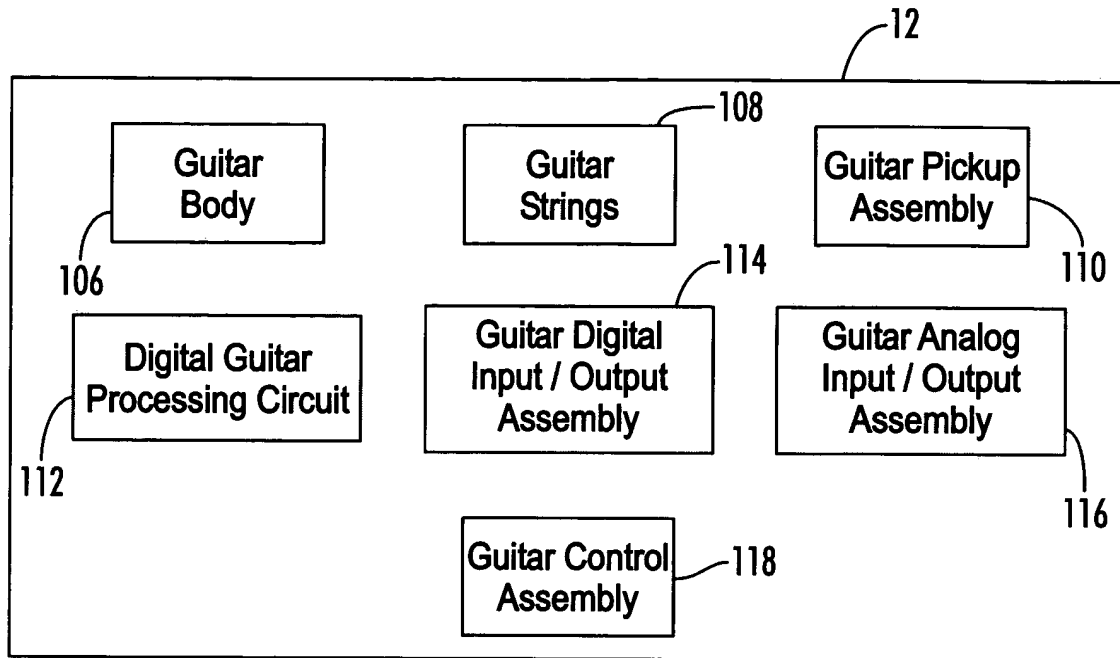


FIG. 5

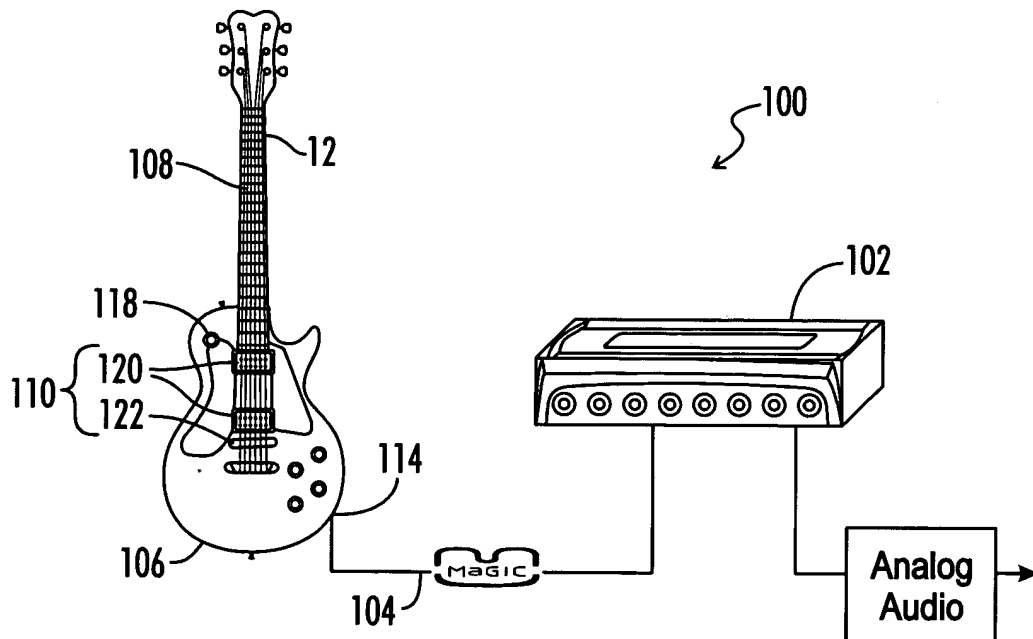


FIG. 6

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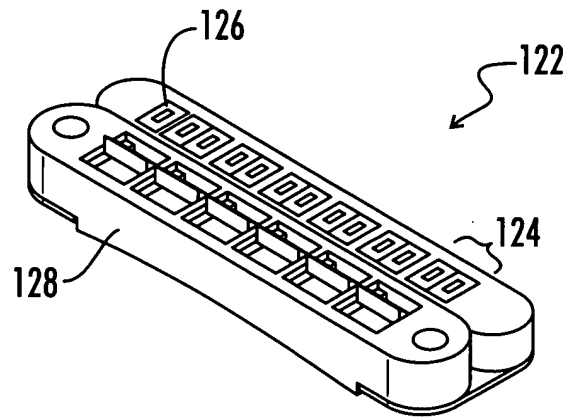


FIG. 7

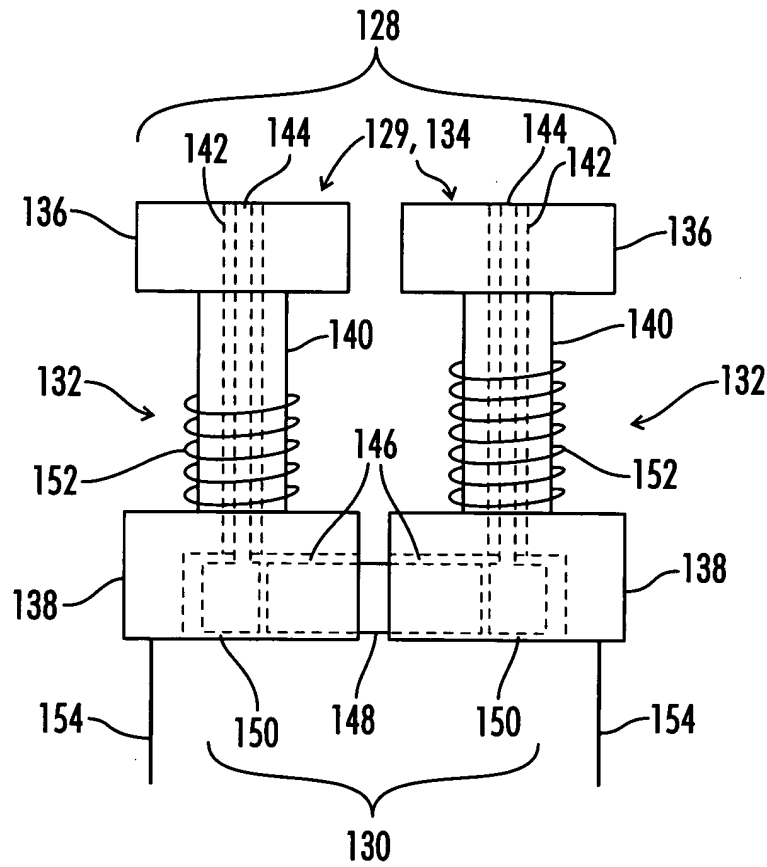


FIG. 8

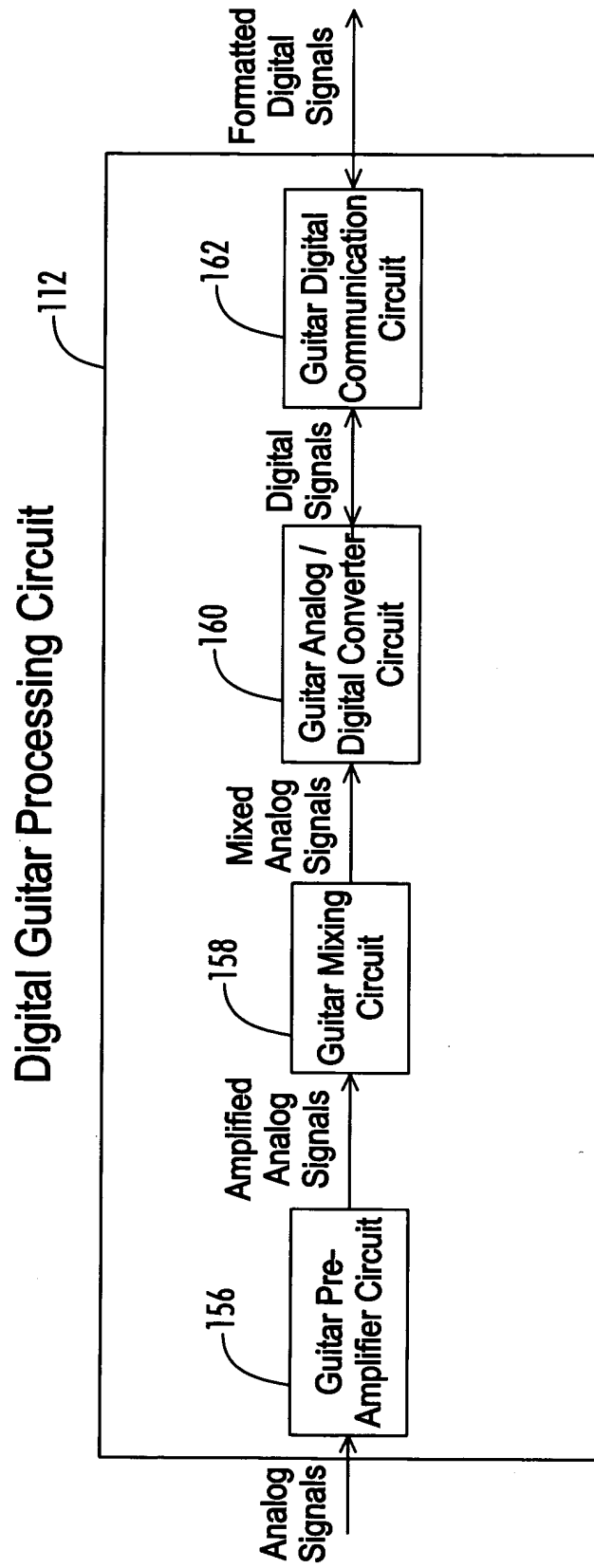


FIG. 9

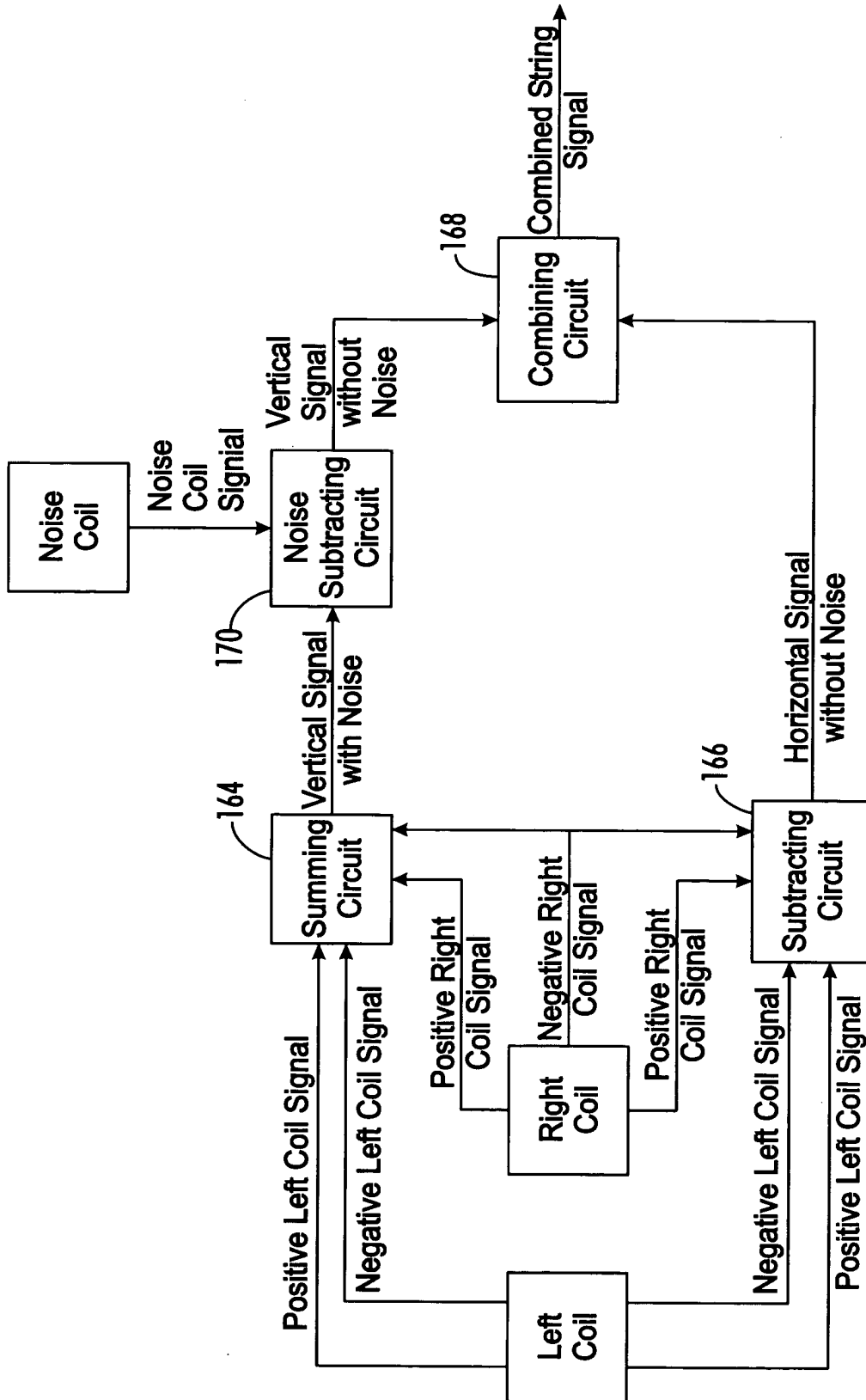


FIG. 10

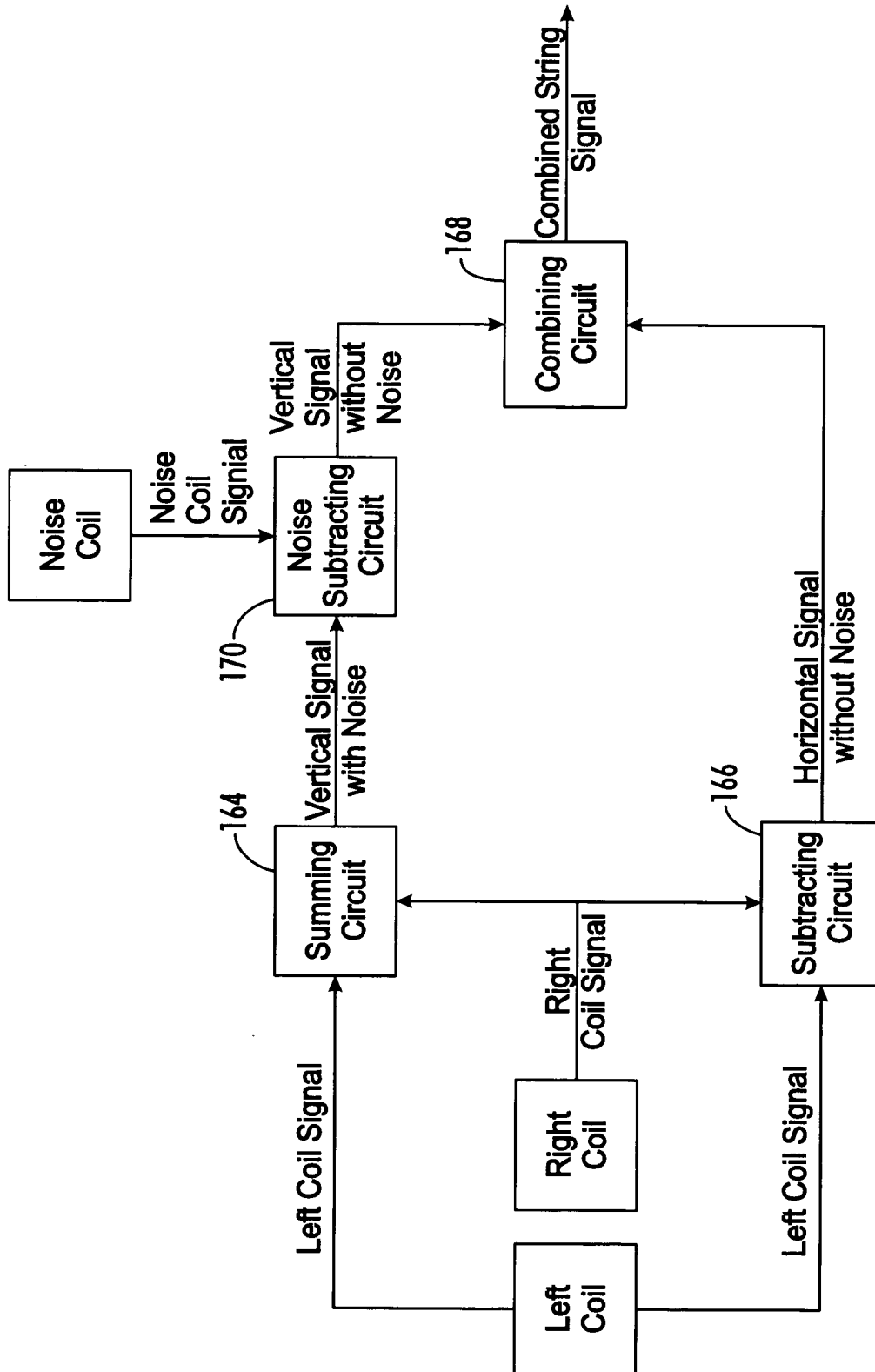


FIG. 11

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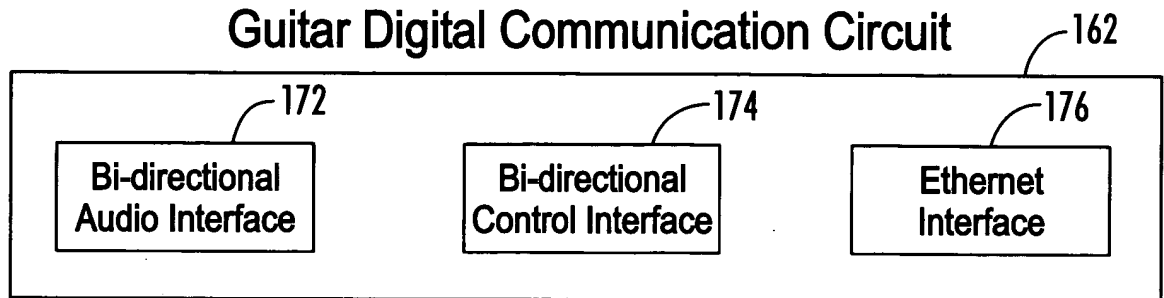


FIG. 12

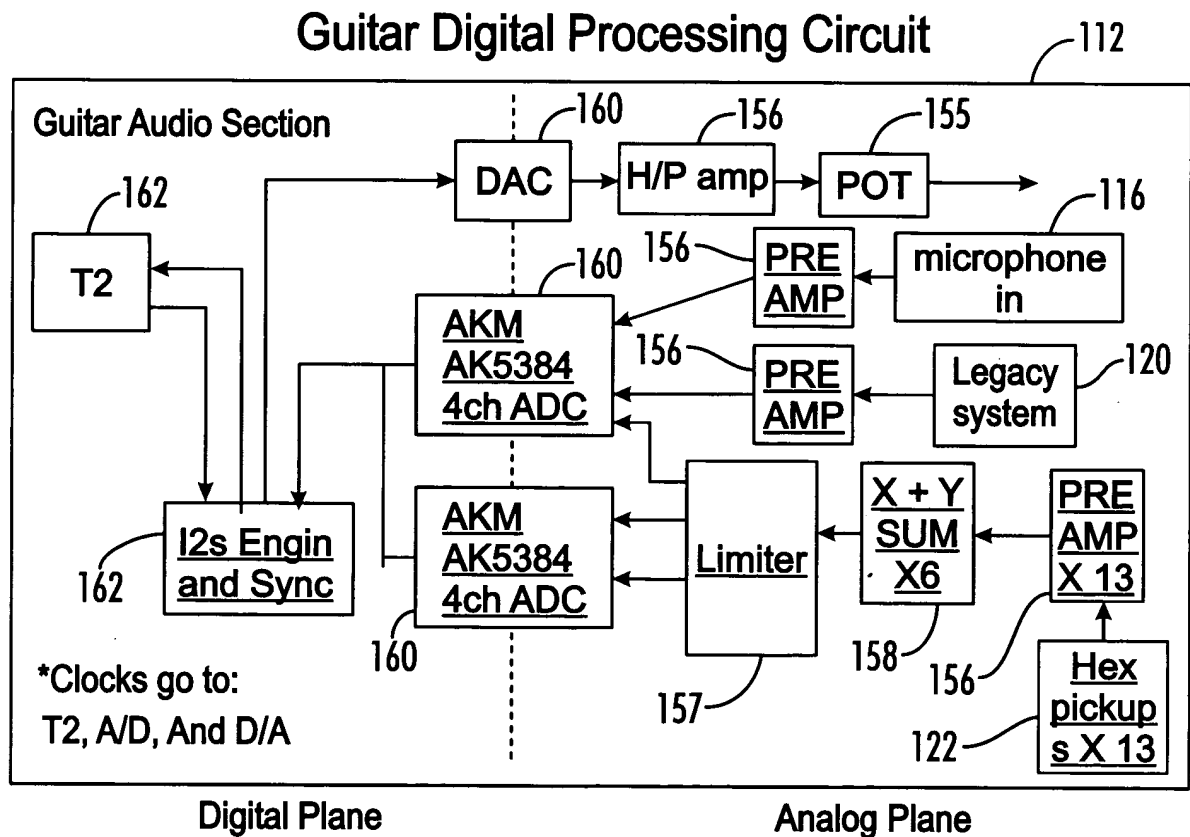


FIG. 13

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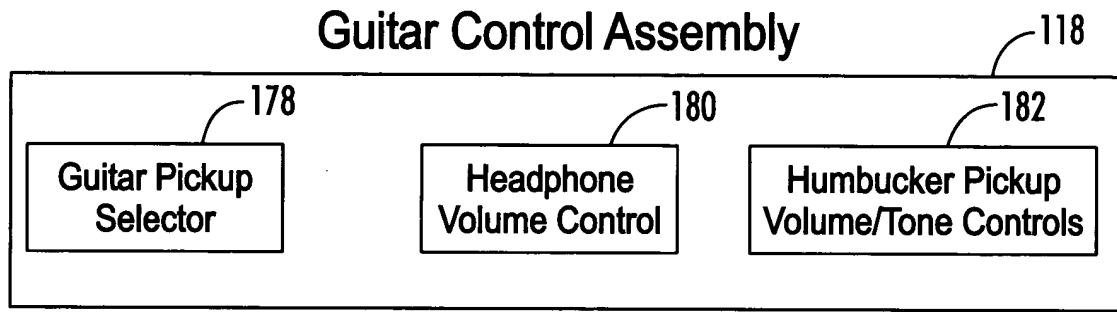


FIG. 14

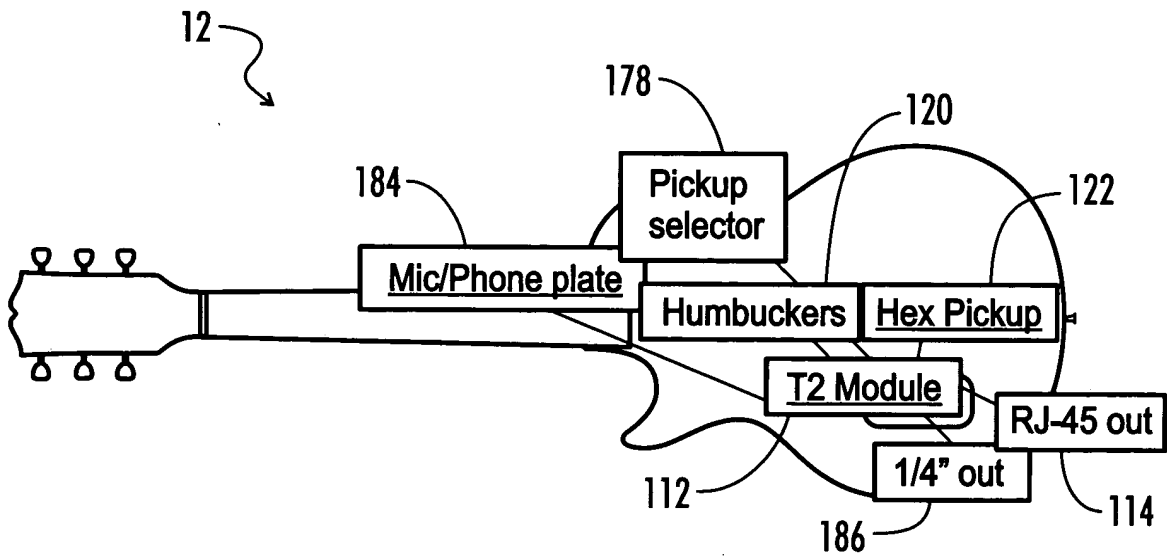


FIG. 15

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Preamp Section

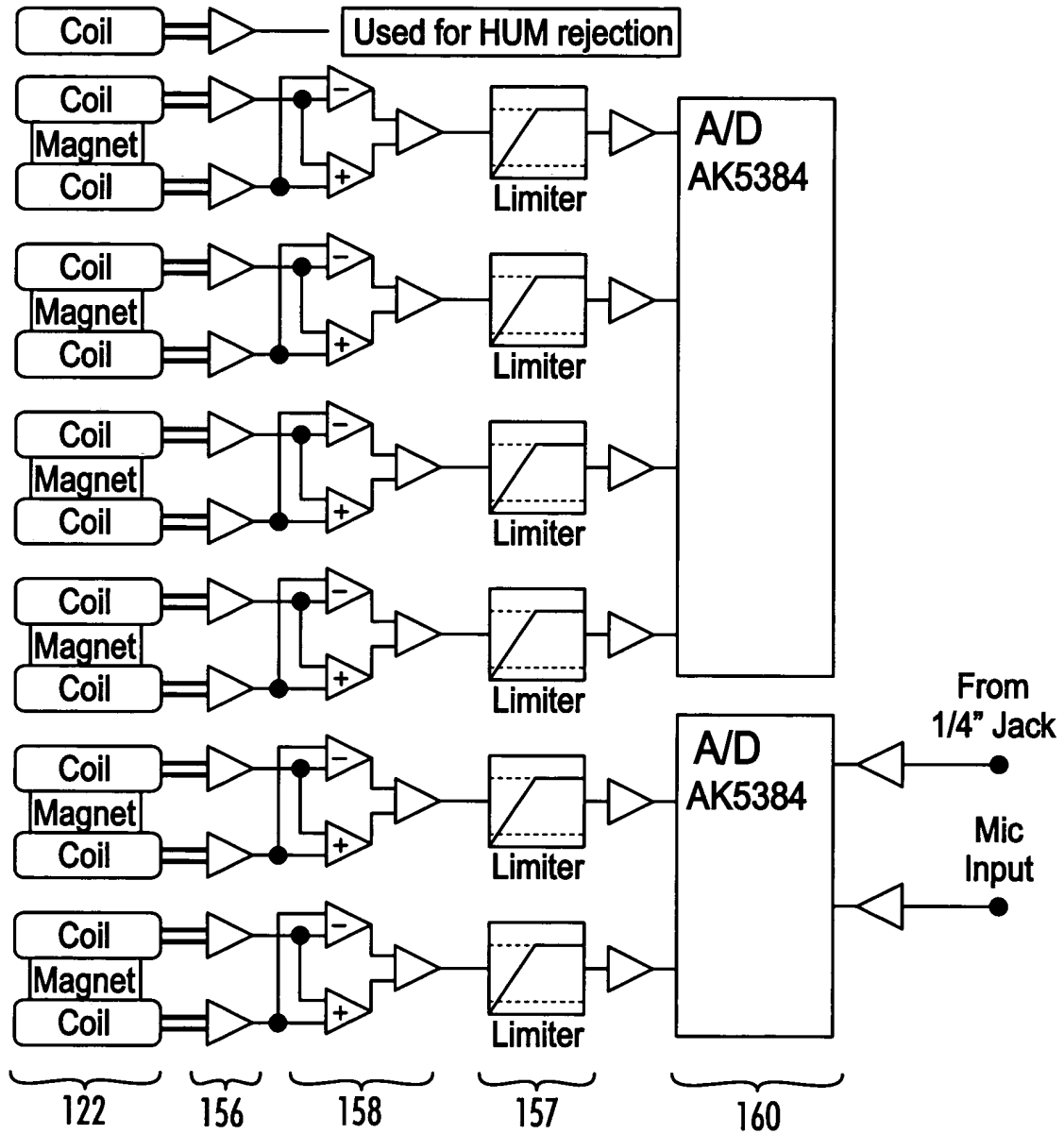


FIG. 16

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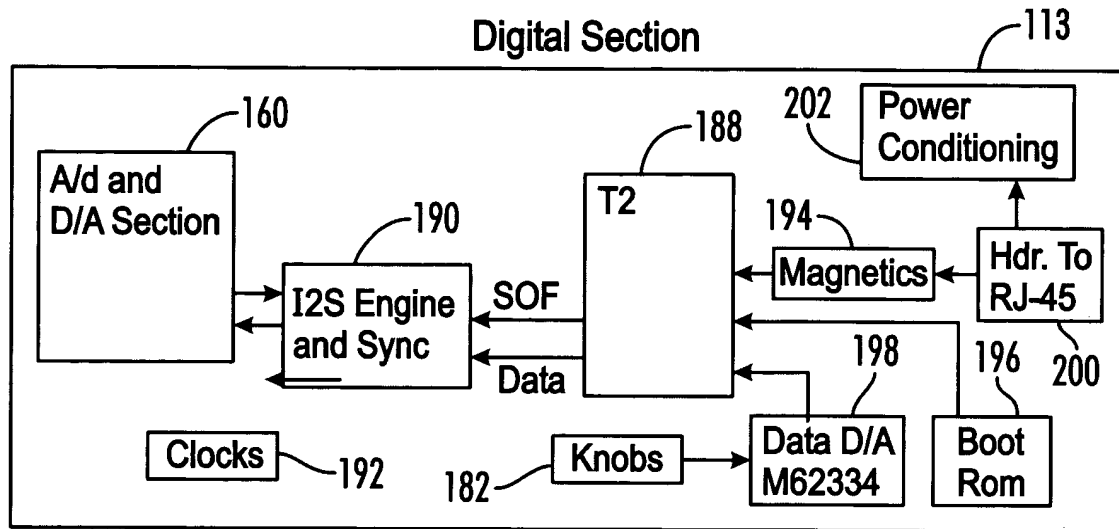


FIG. 17

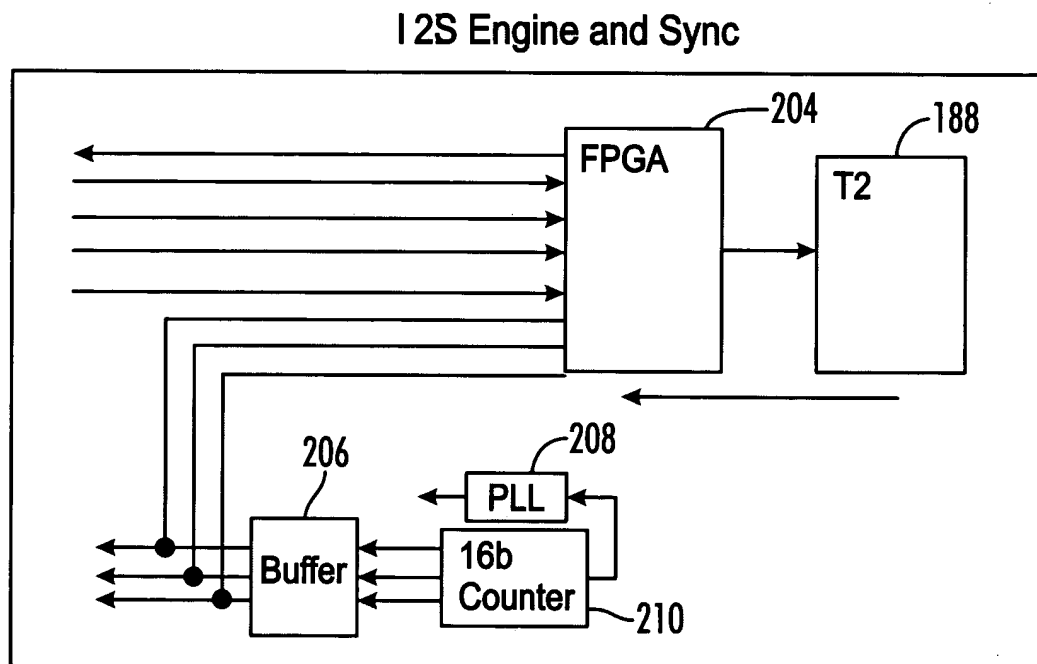


FIG. 18

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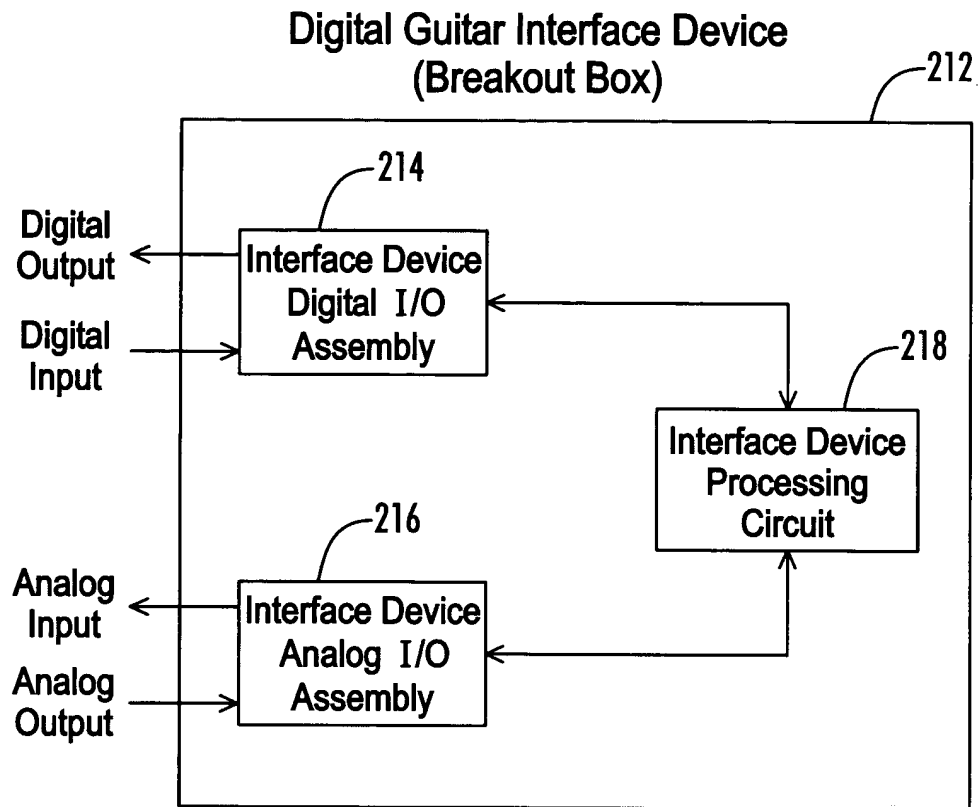


FIG. 19

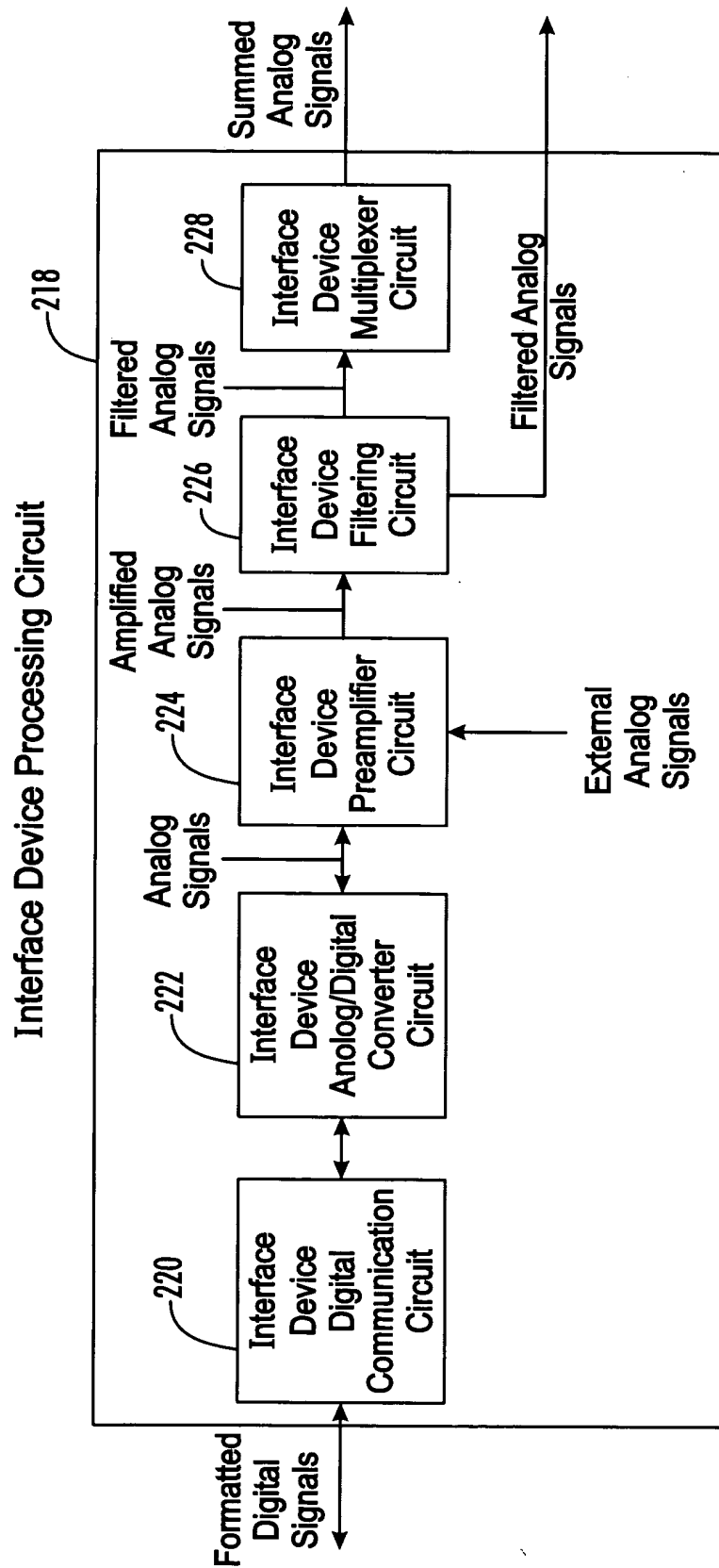
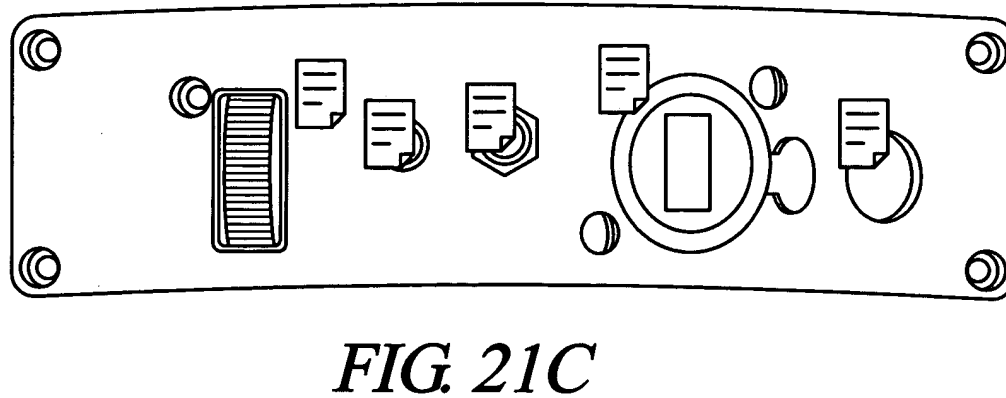
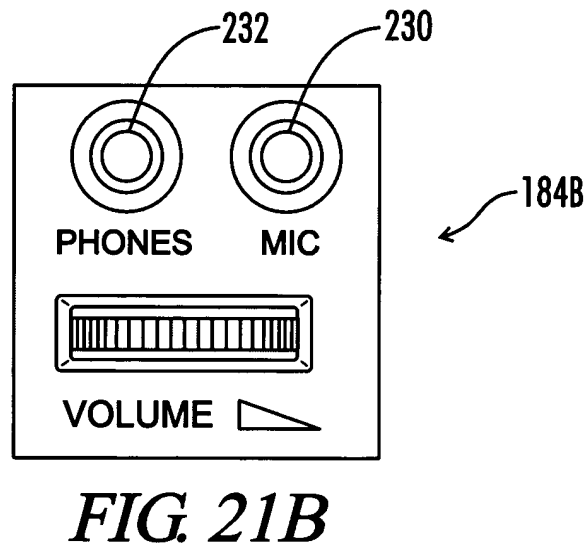
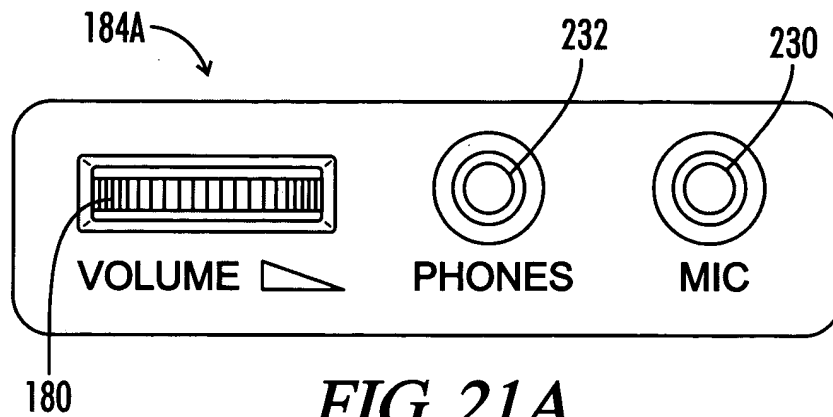


FIG. 20

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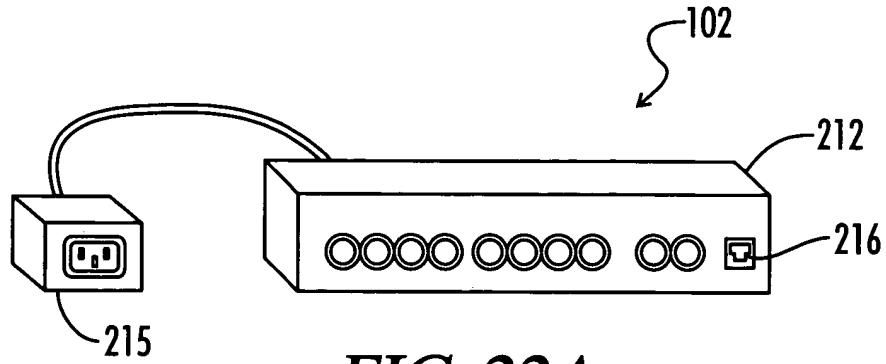


FIG. 22A

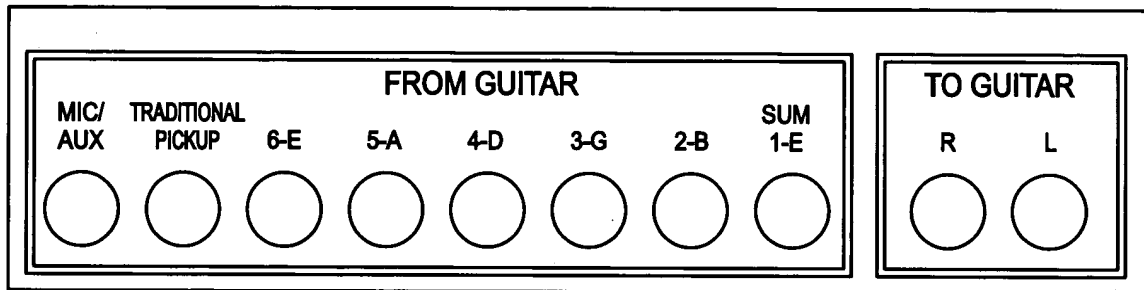


FIG. 22B

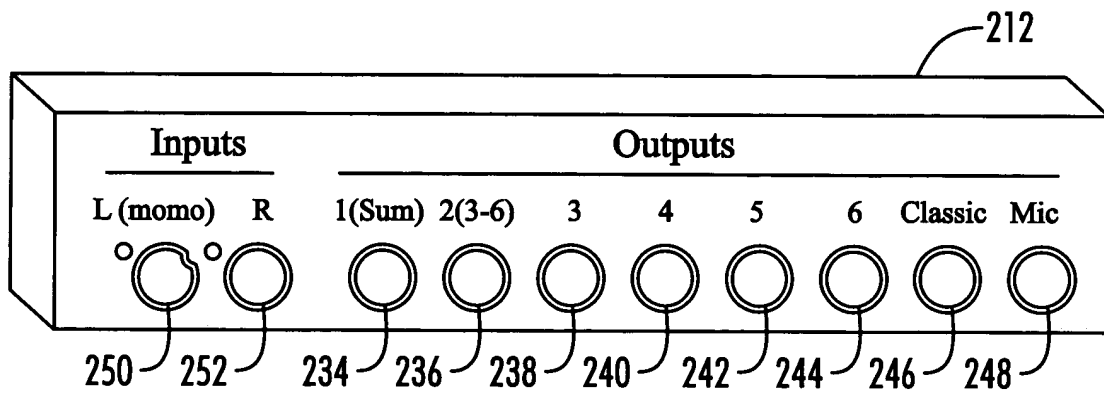


FIG. 23

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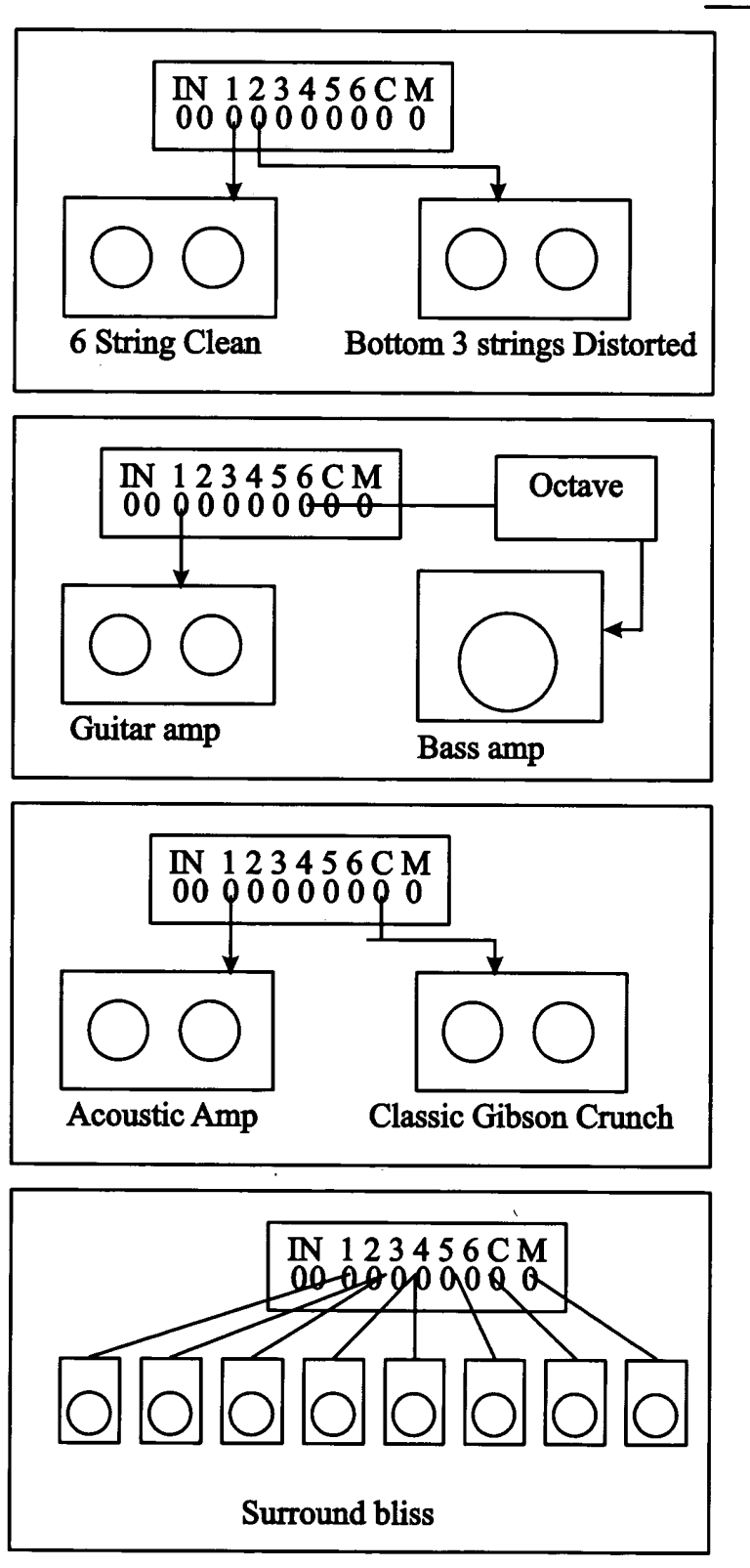


FIG. 24

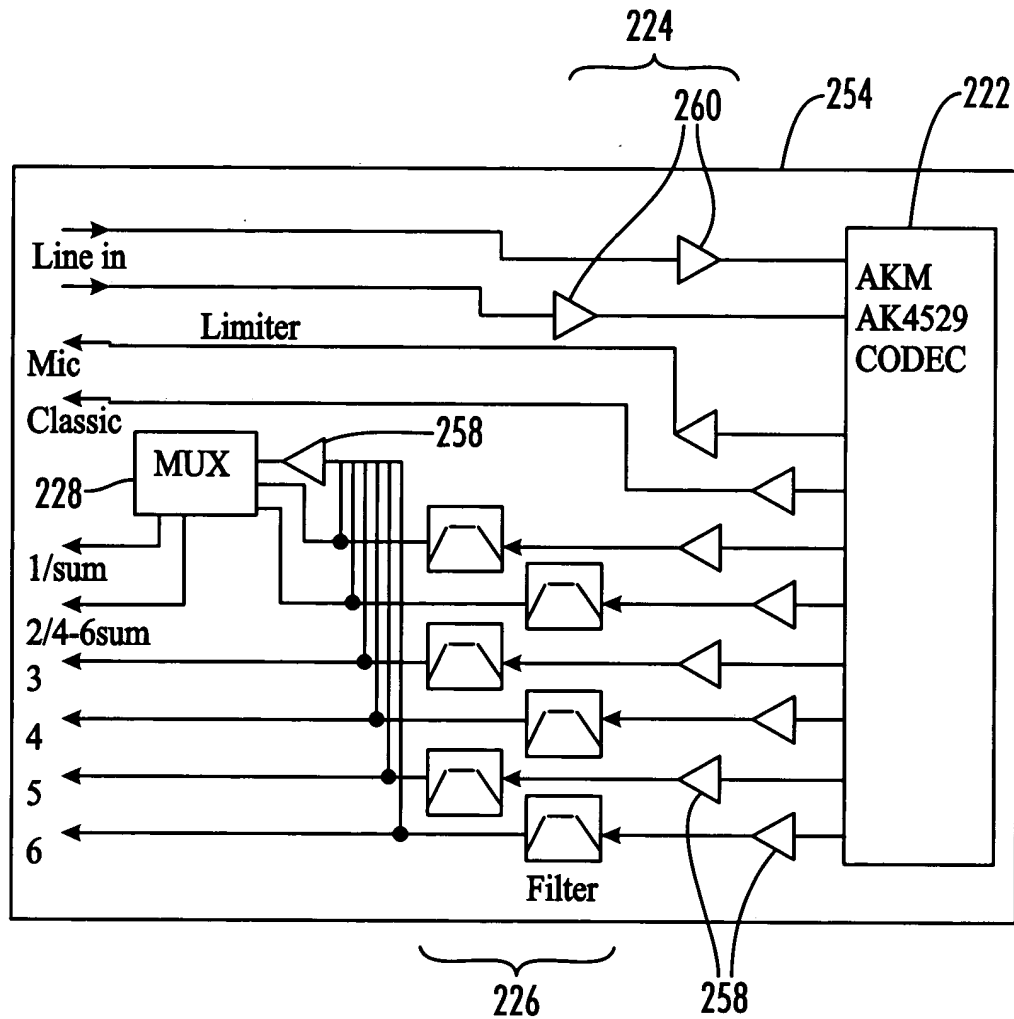


FIG. 25

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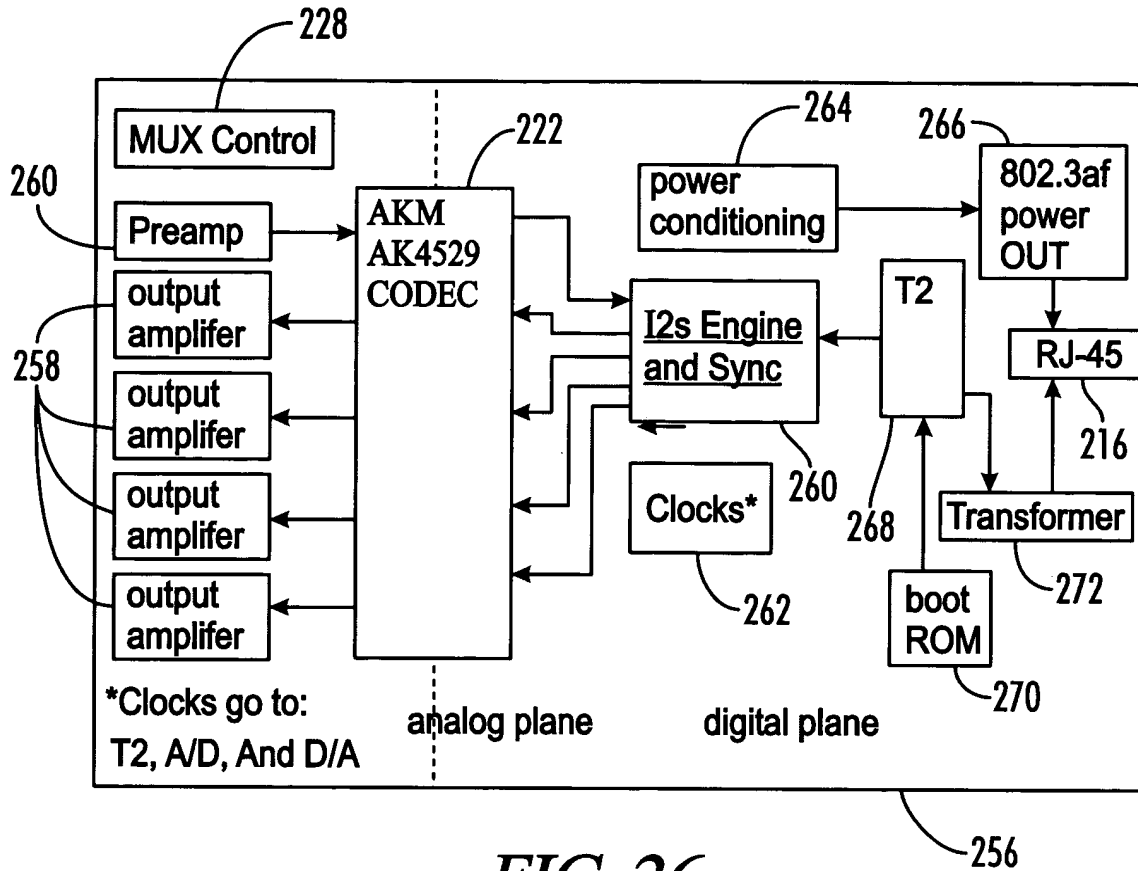


FIG. 26

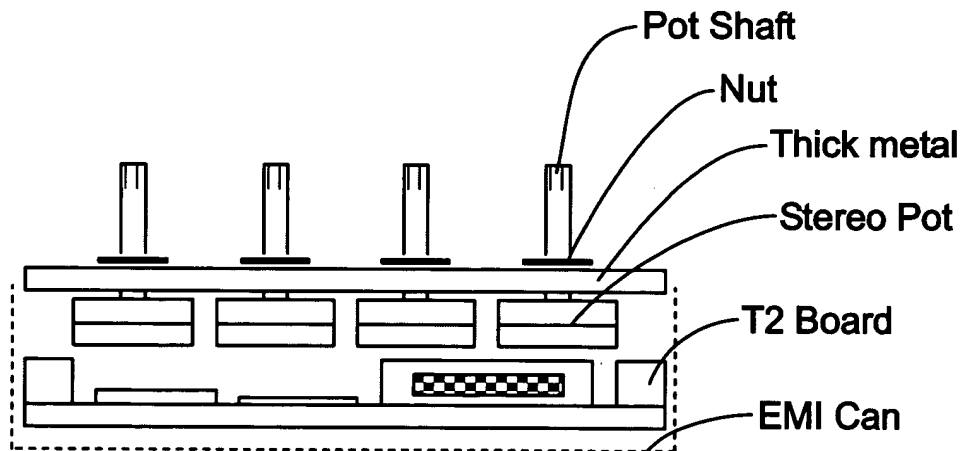


FIG. 27

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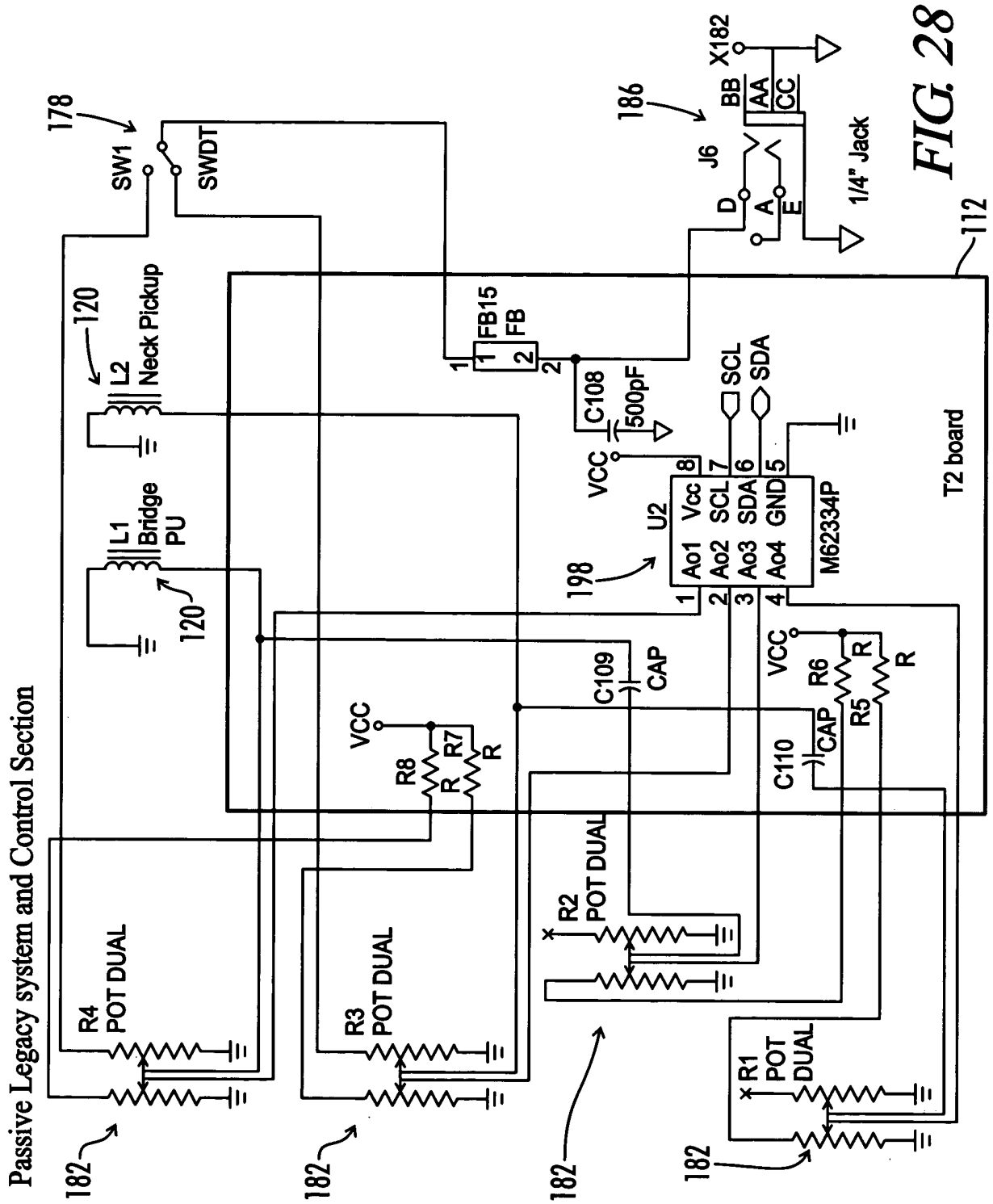


FIG 28

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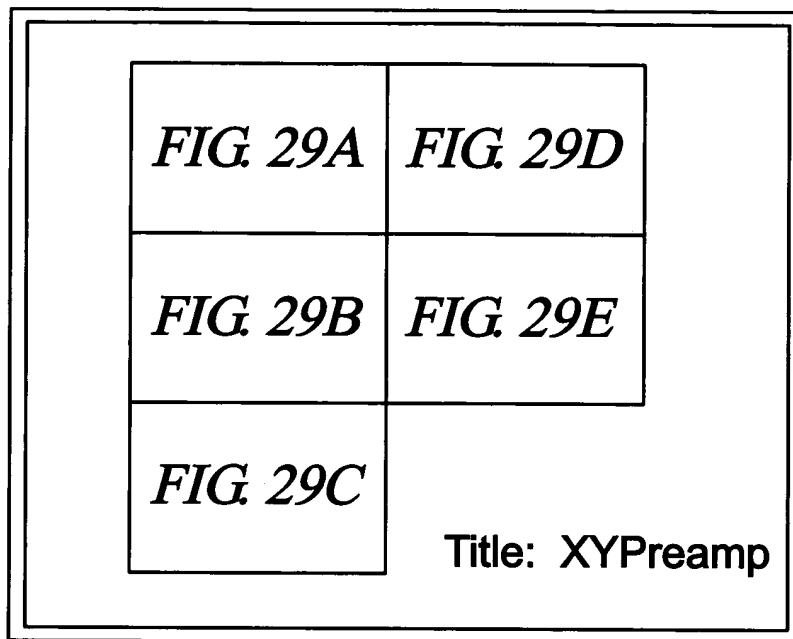
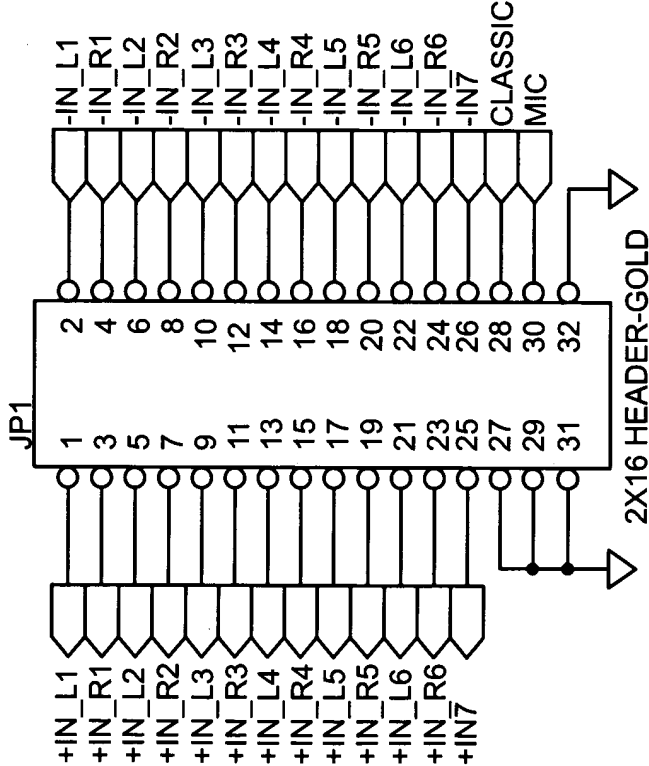


FIG. 29

+

Pickup & Mic Inputs
Connector to be specified



HEX Pickup

+IN_L1, -IN_L1, +IN_R1, -IN_R1 = High E string
+IN_L2, -IN_L2, +IN_R2, -IN_R2 = B string
+IN_L3, -IN_L3, +IN_R3, -IN_R3 = G string
+IN_L4, -IN_L4, +IN_R4, -IN_R4 = D string
+IN_L5, -IN_L5, +IN_R5, -IN_R5 = A string
+IN_L6, -IN_L6, +IN_R6, -IN_R6 = Low E string
+IN_7, -IN_7 = Hum cancellation coil

CLASSIC = Hum Bucker pickup (Classic is actually input from control board. The header connection will be an output to the 1/4" jack)

MIC = microphone input (this input has 12V phantom power for capacitor microphones)

FIG. 29A

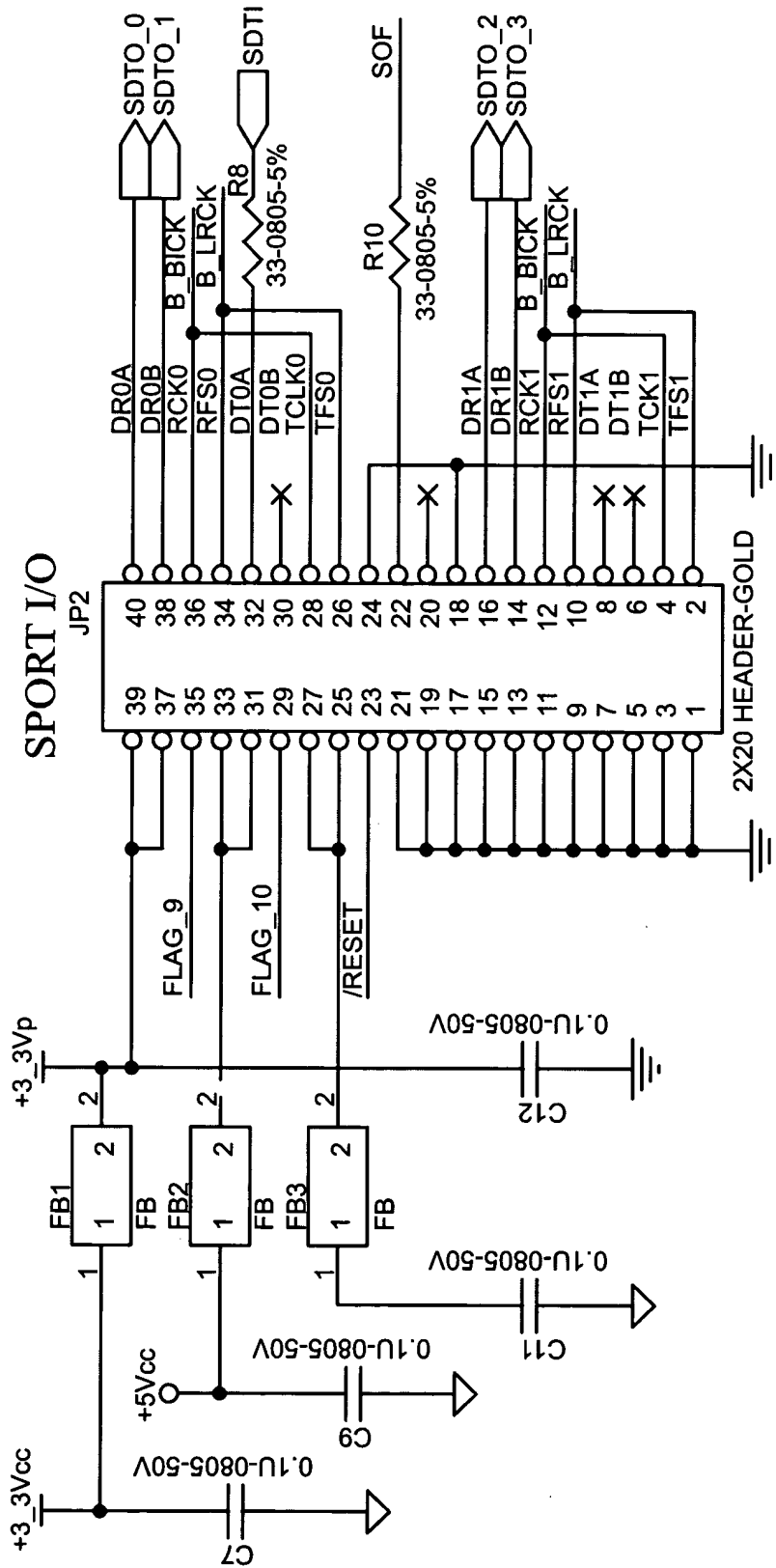


FIG. 29B

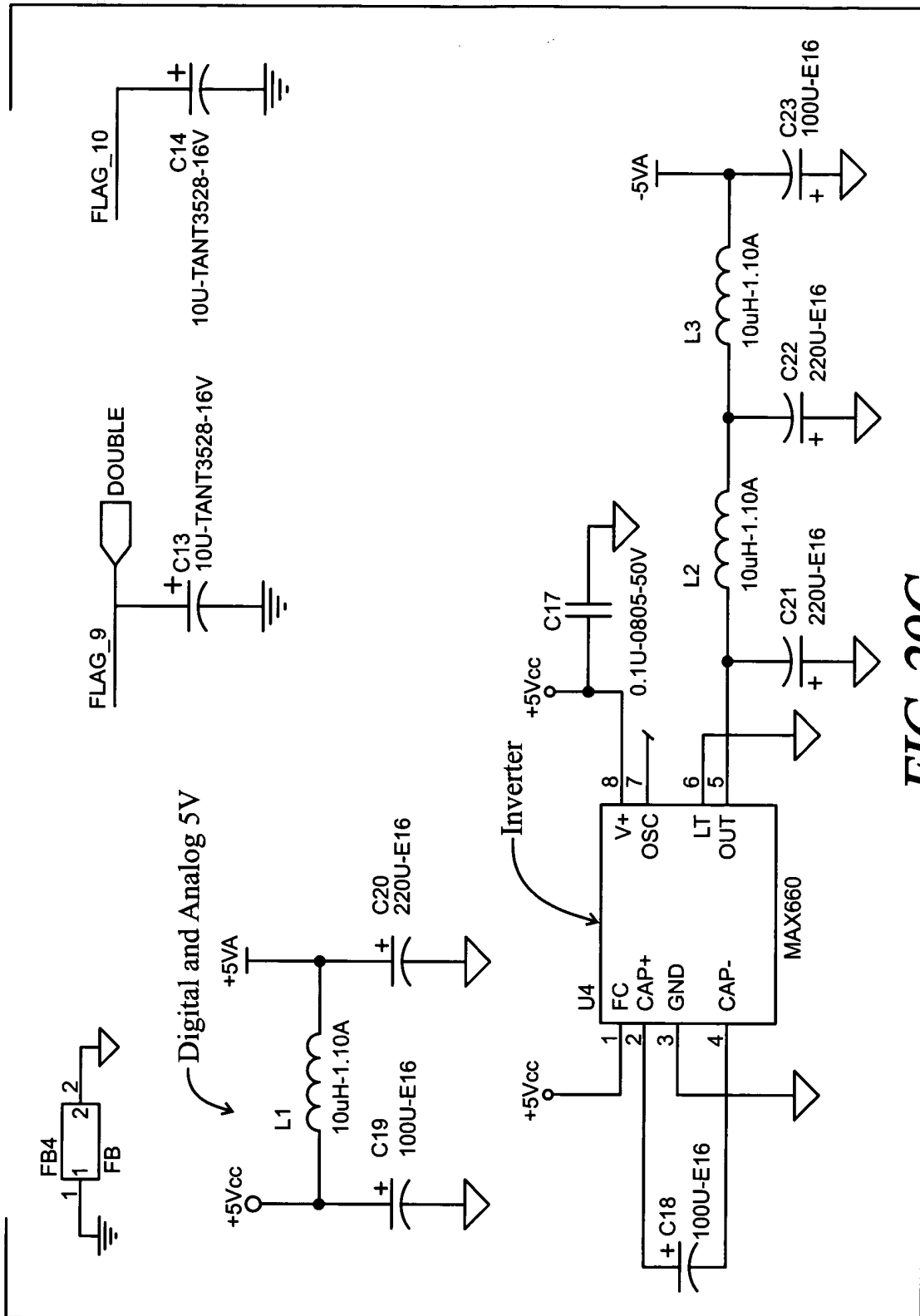
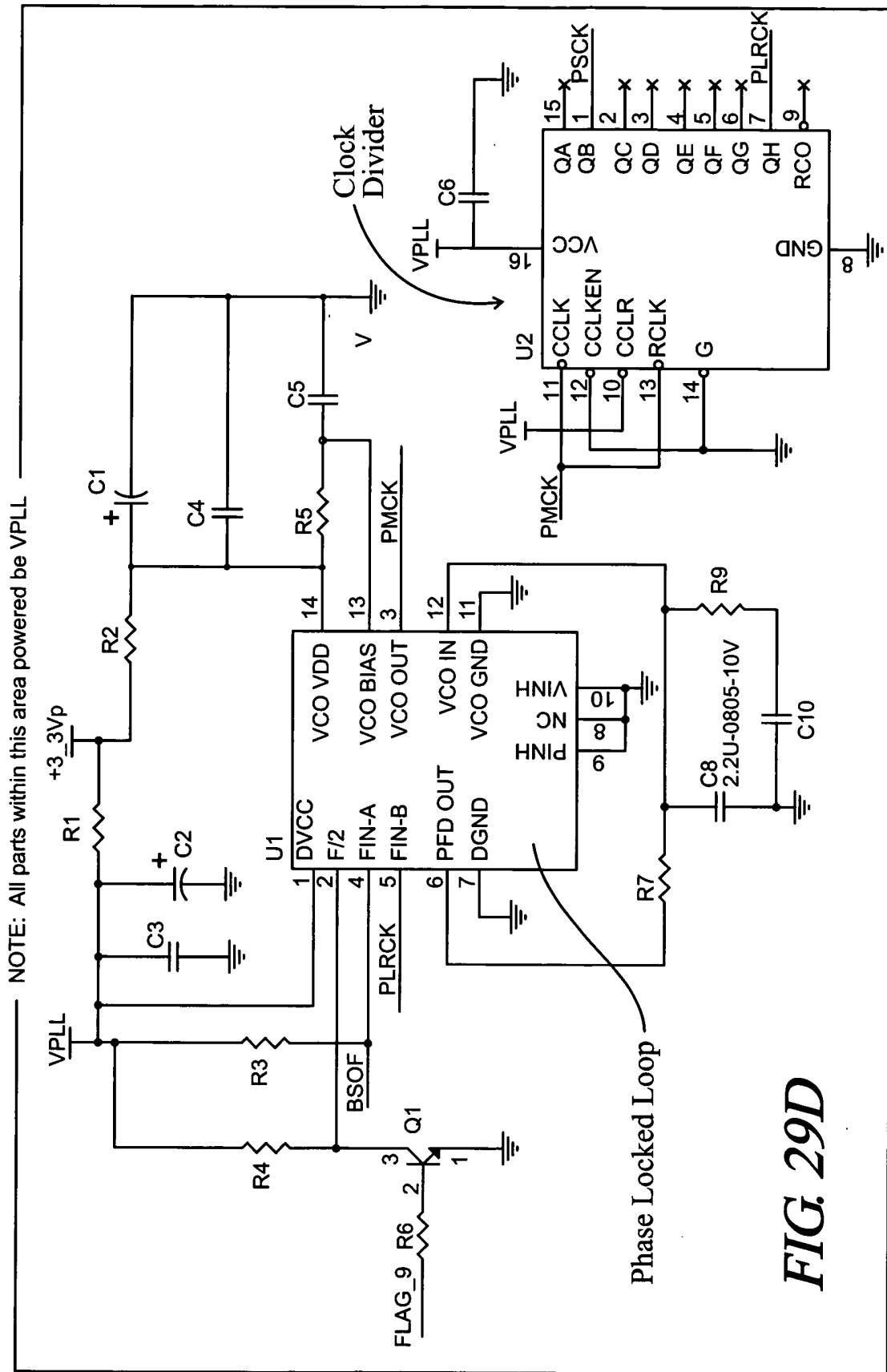


FIG. 29C



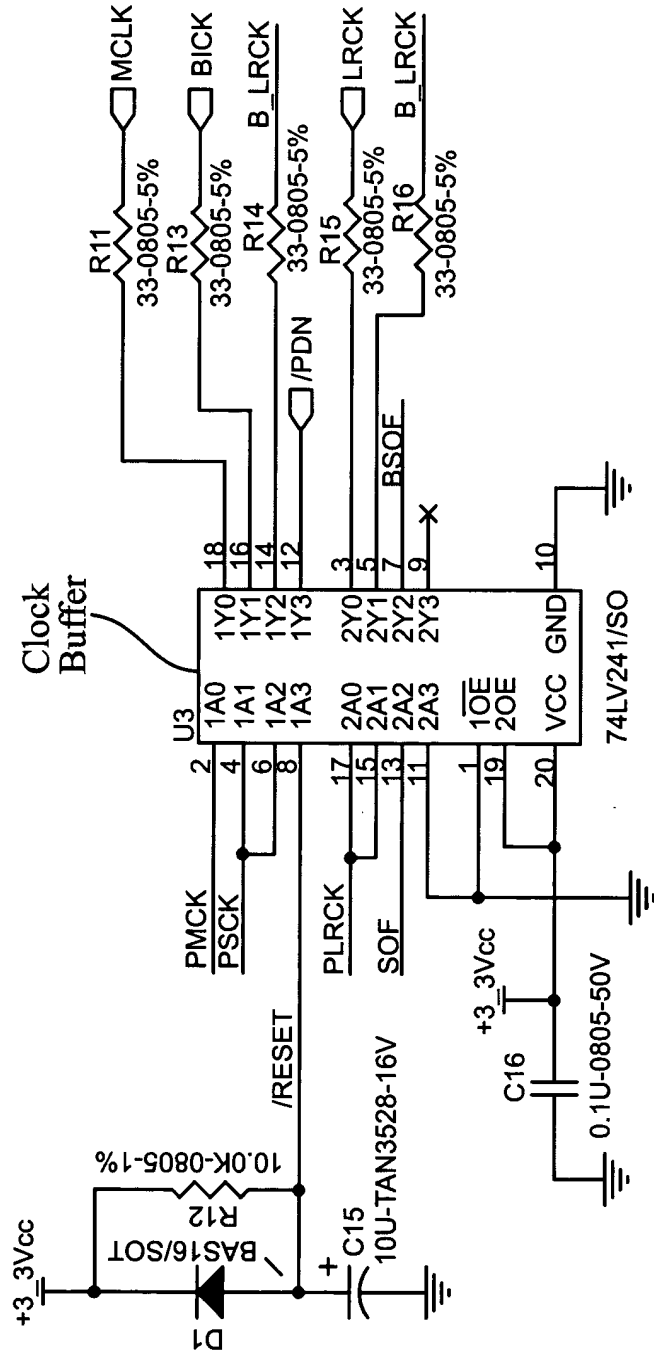


FIG. 29E

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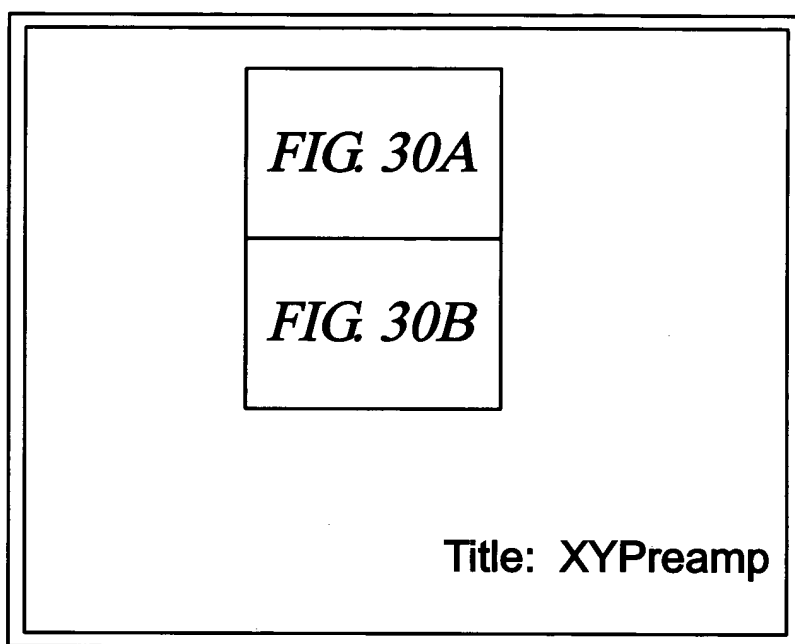
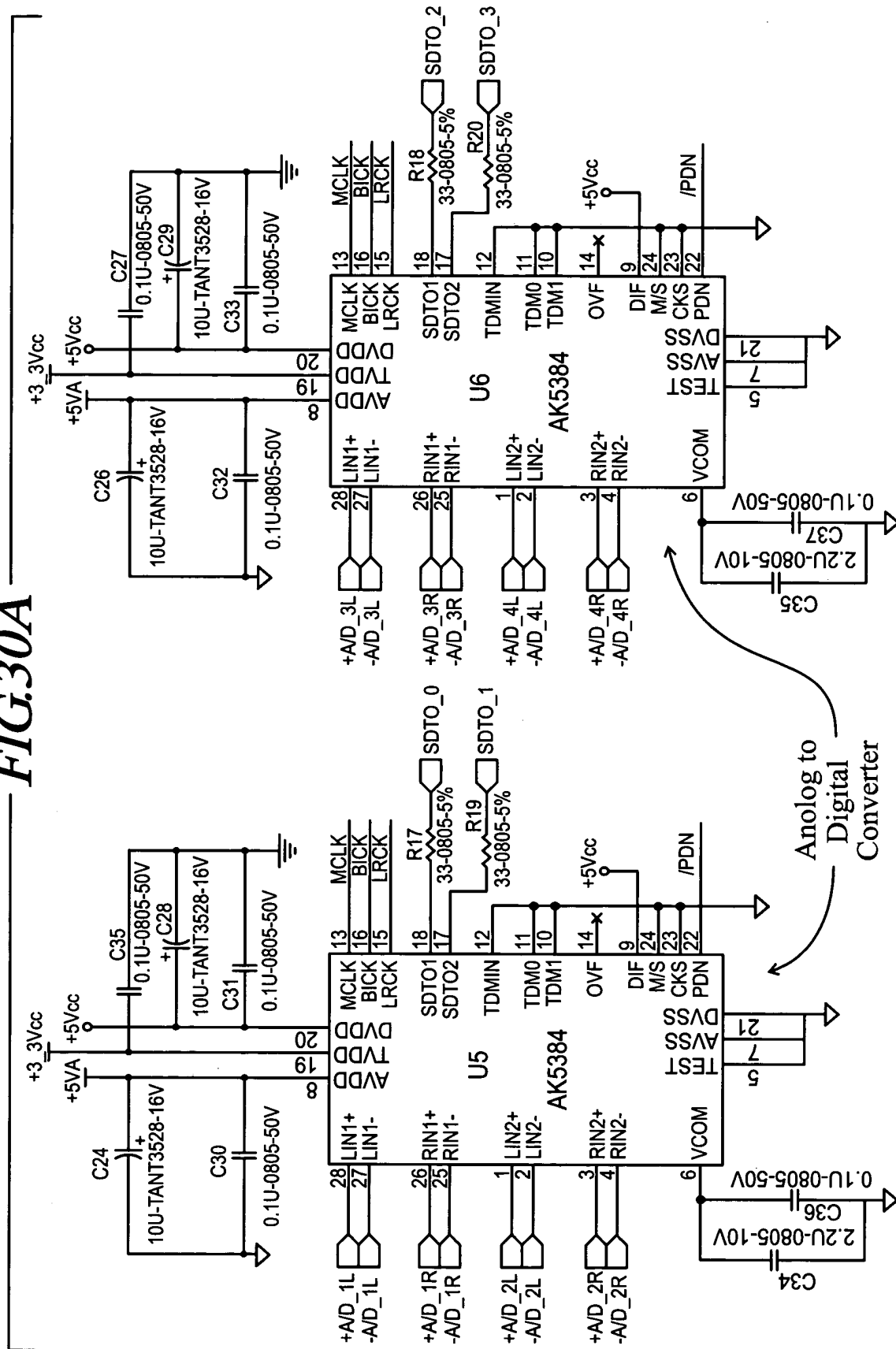


FIG. 30

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FIG.30A



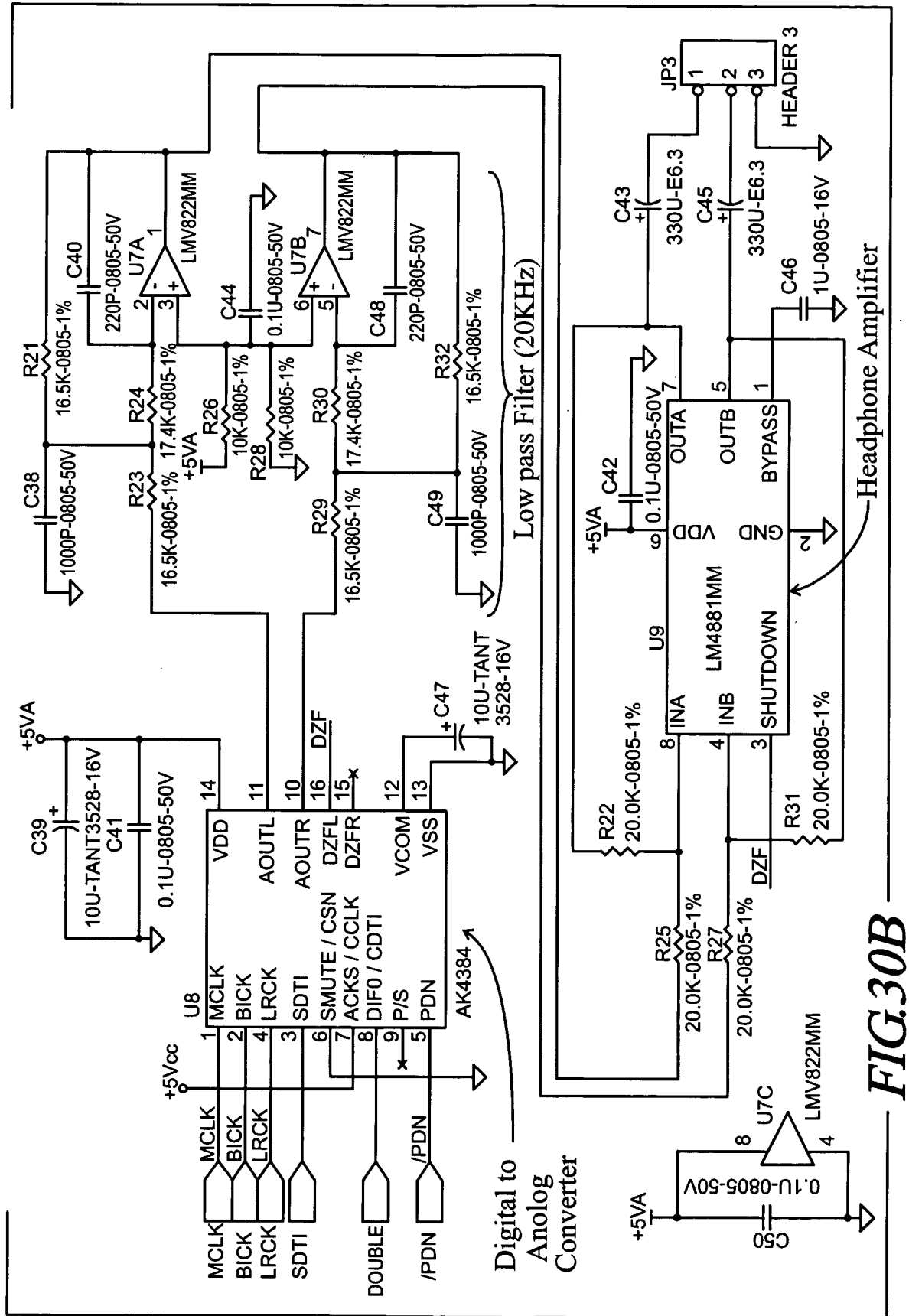


FIG.30B

+

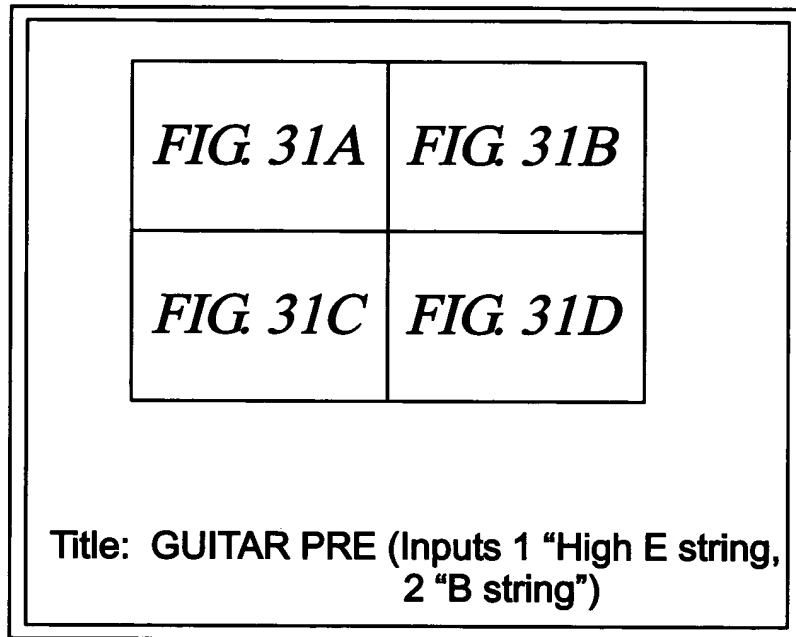


FIG. 31

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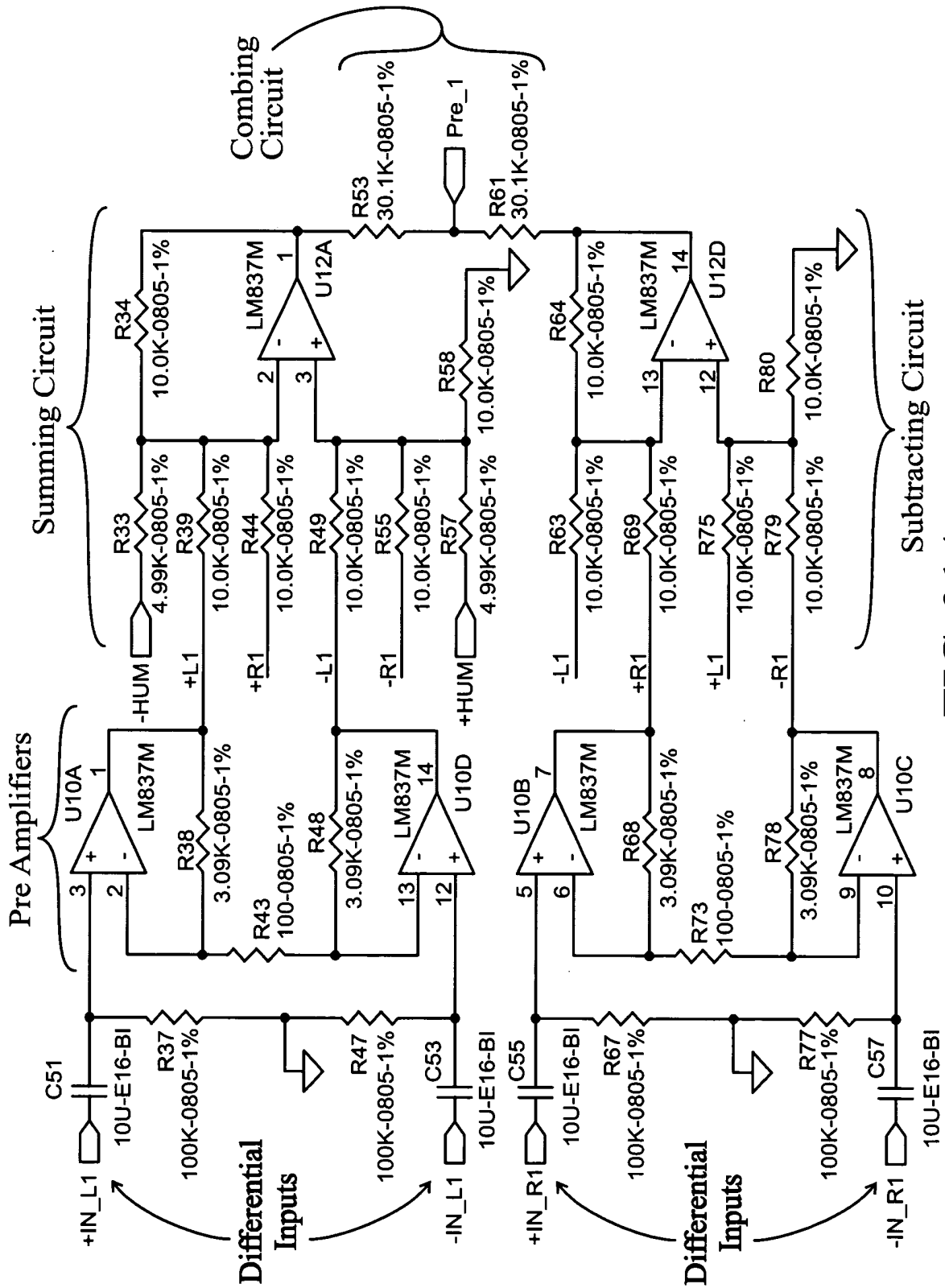


FIG. 31A



+

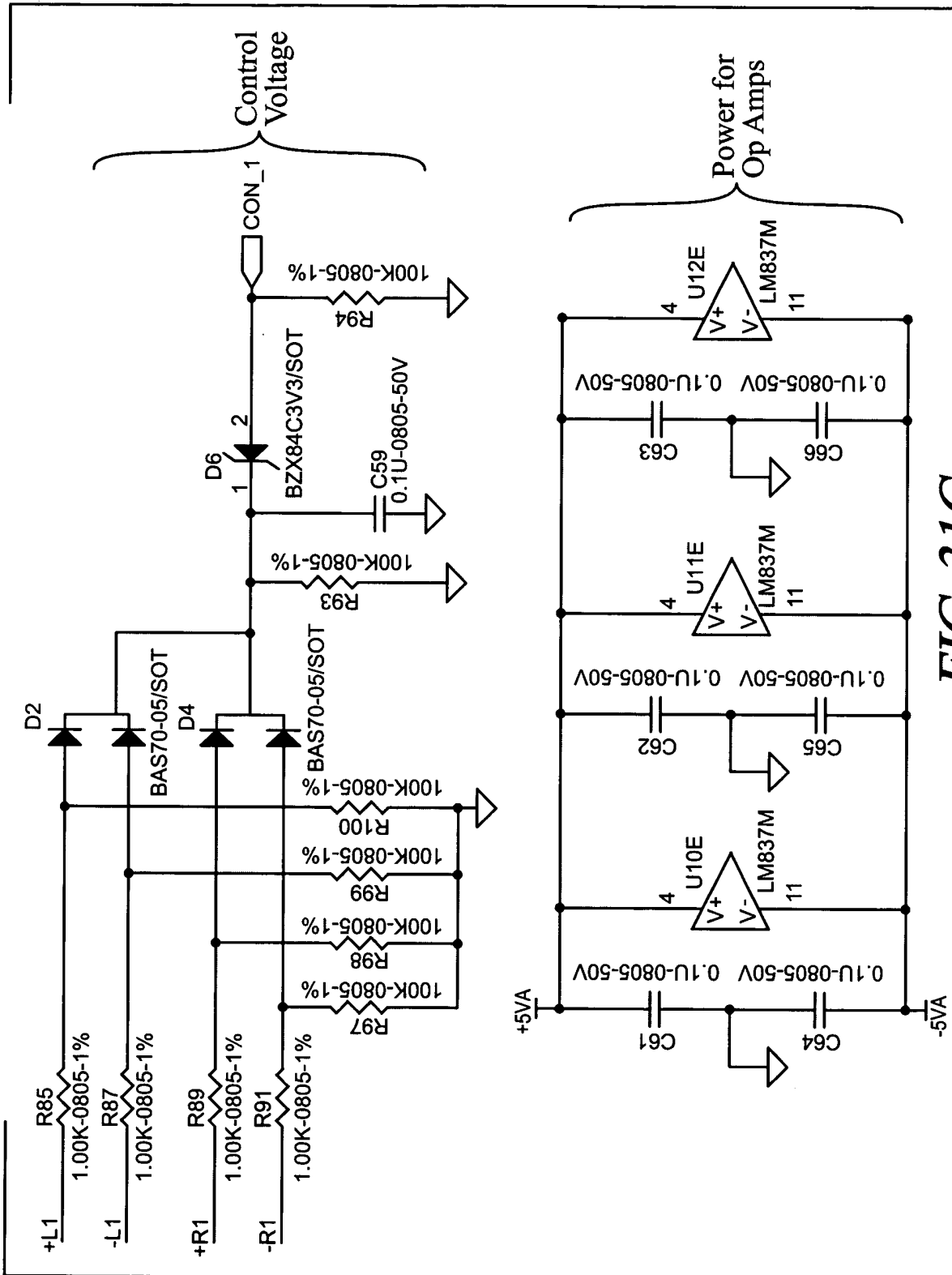


FIG 31C



FIG. 31D

+

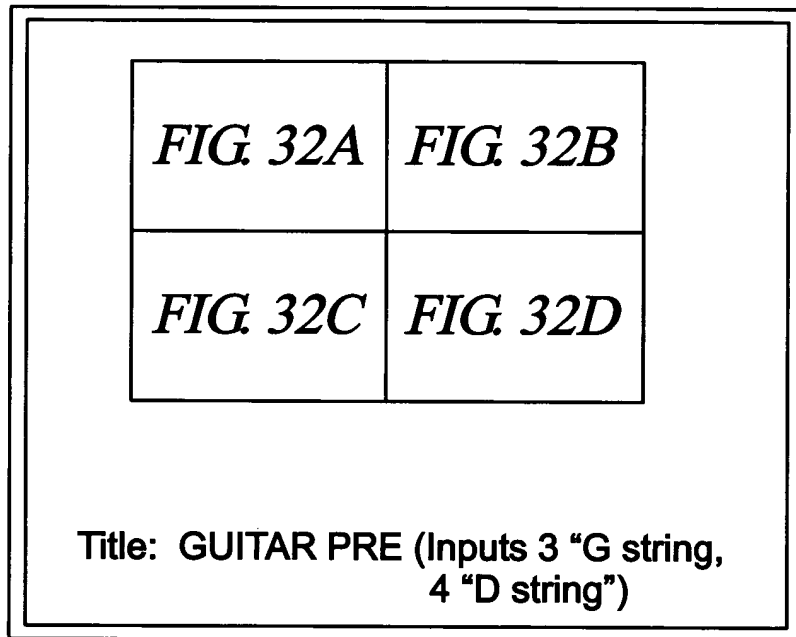


FIG. 32

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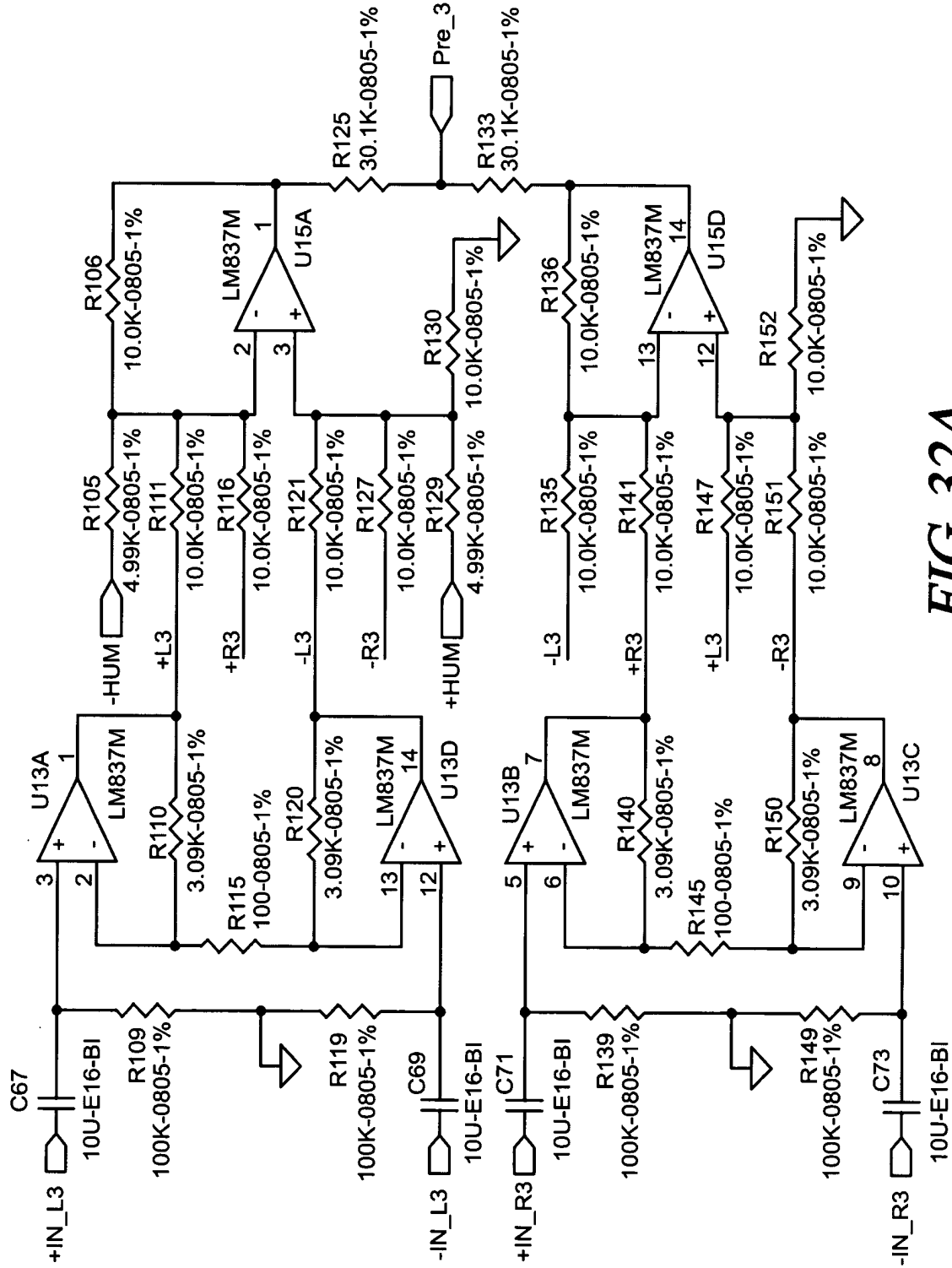
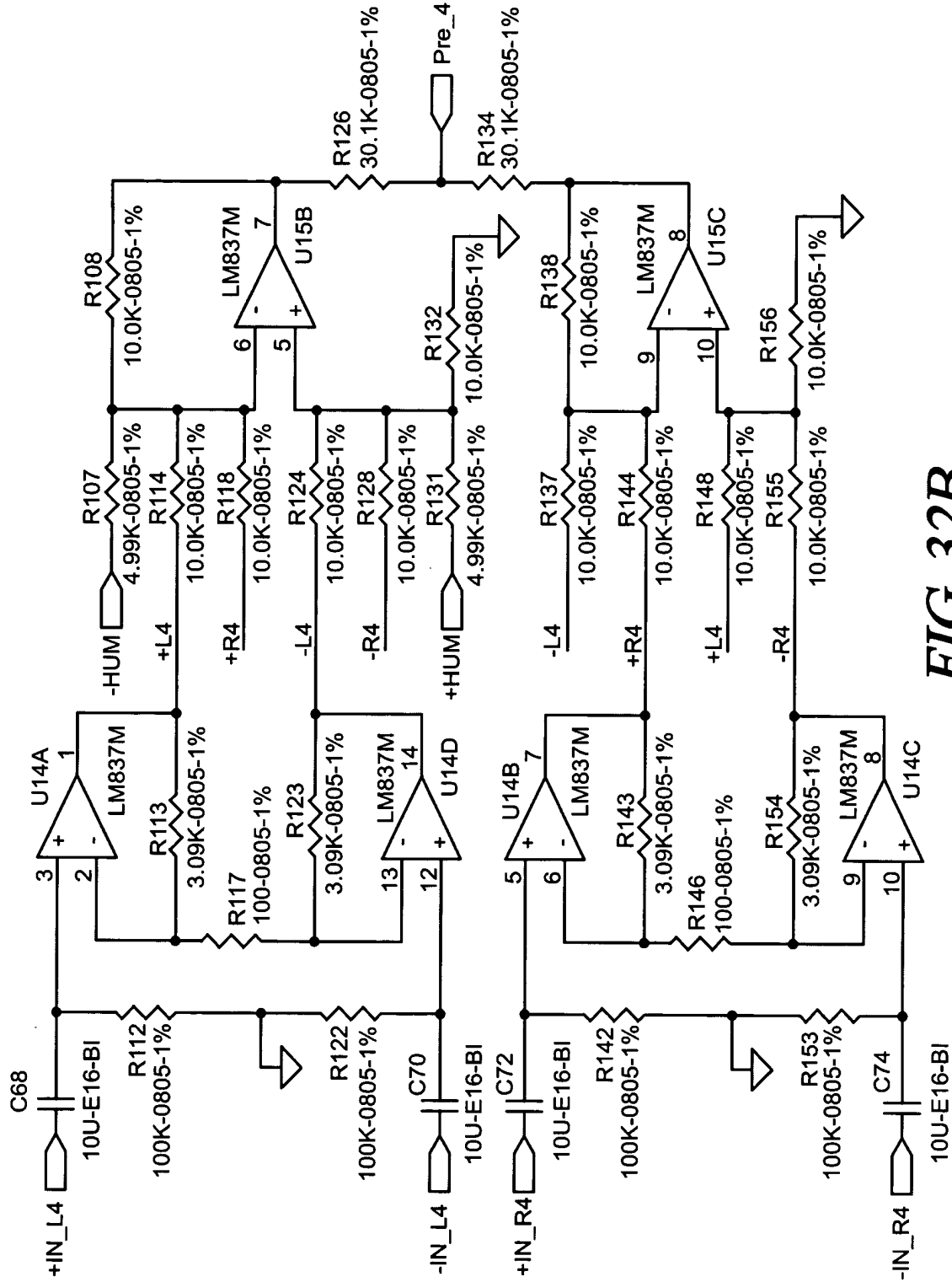


FIG 32A



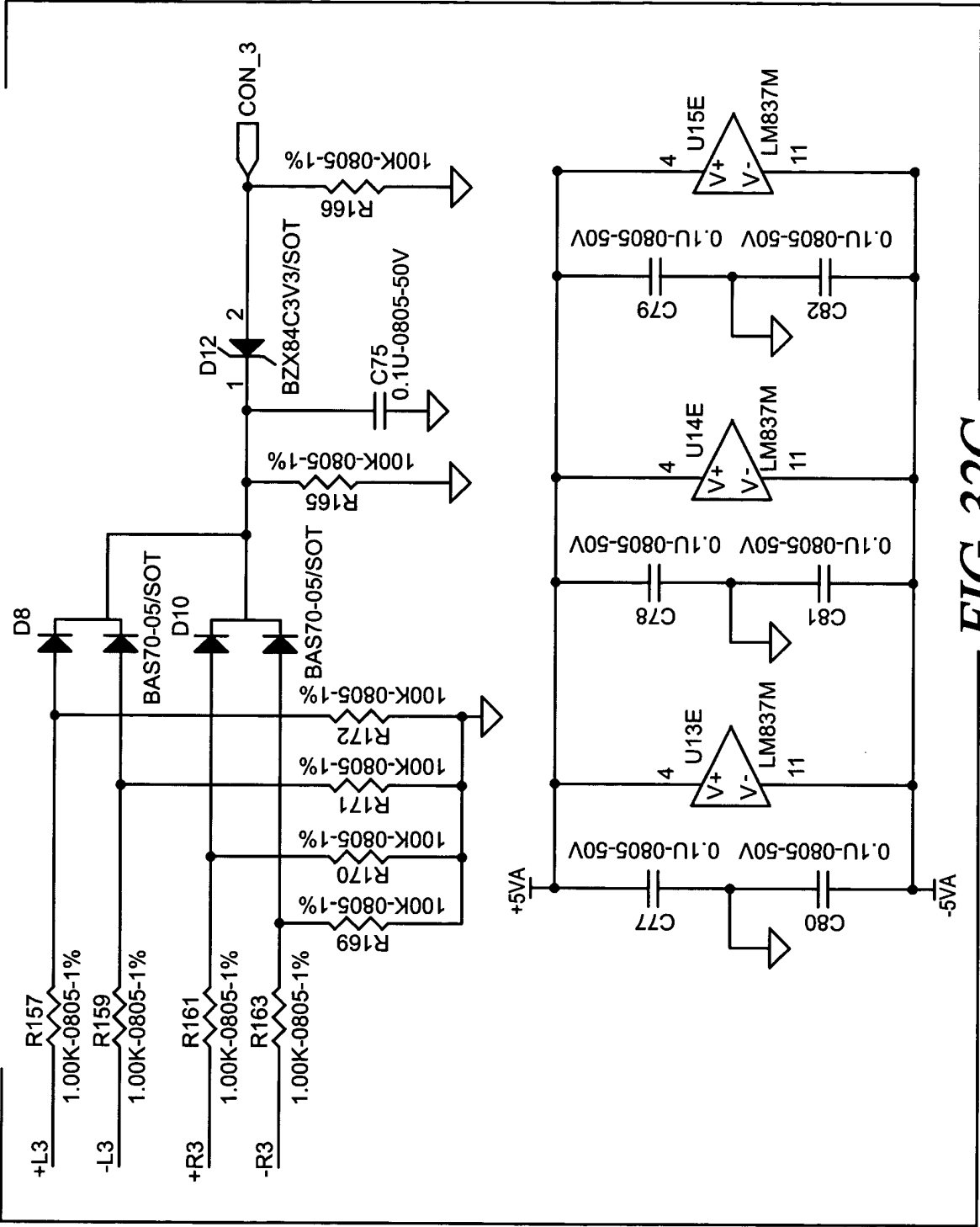


FIG 32C



FIG. 32D

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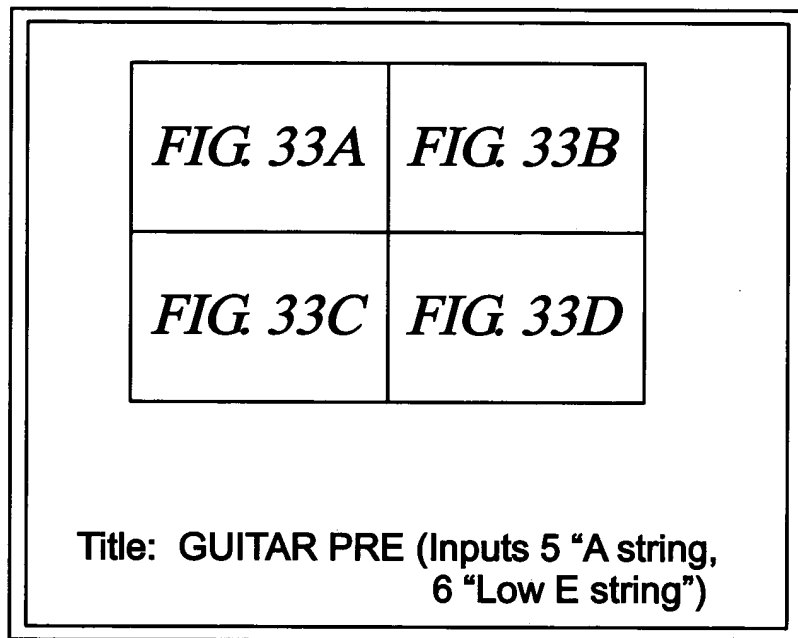


FIG. 33

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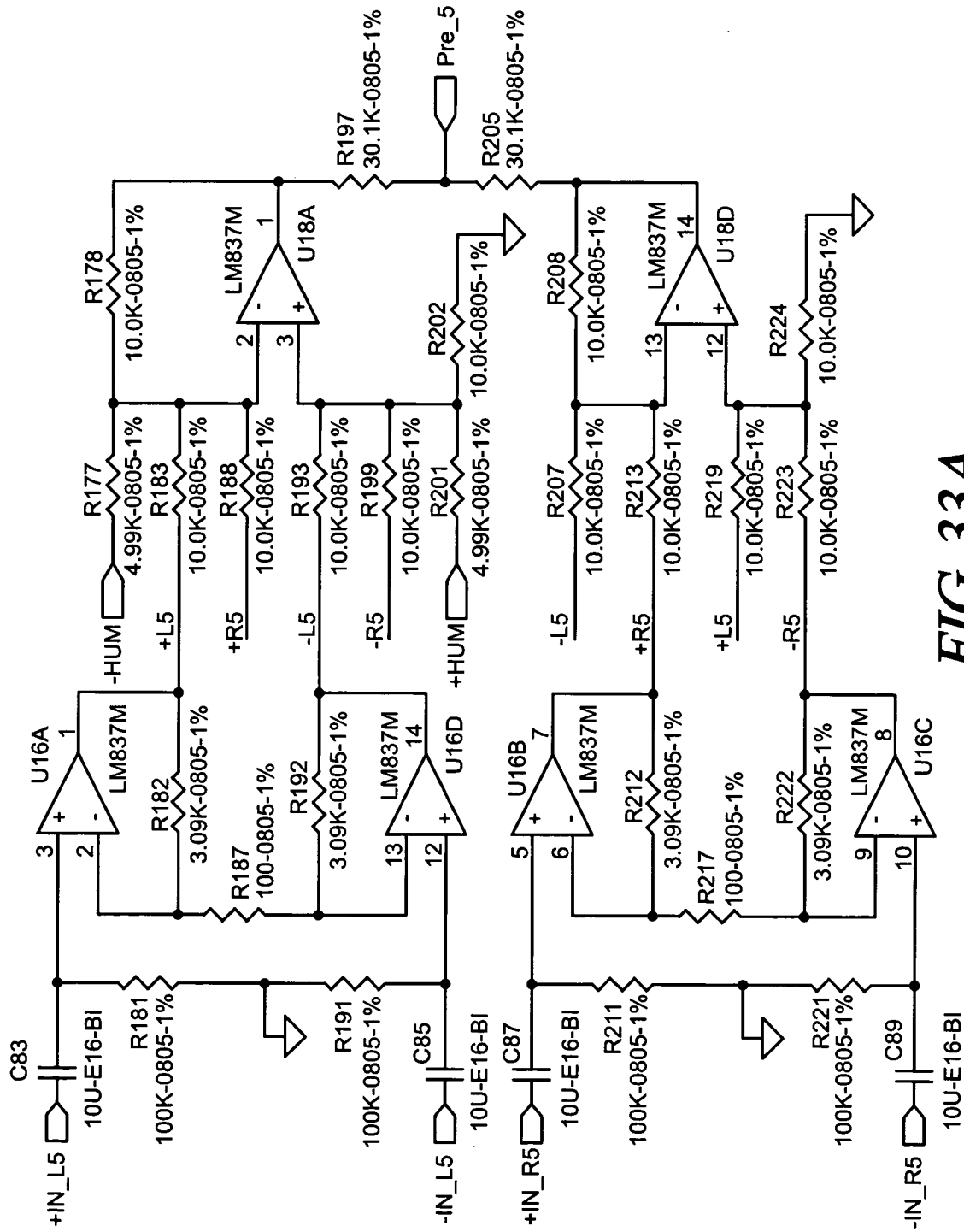


FIG. 33A



FIG. 33B

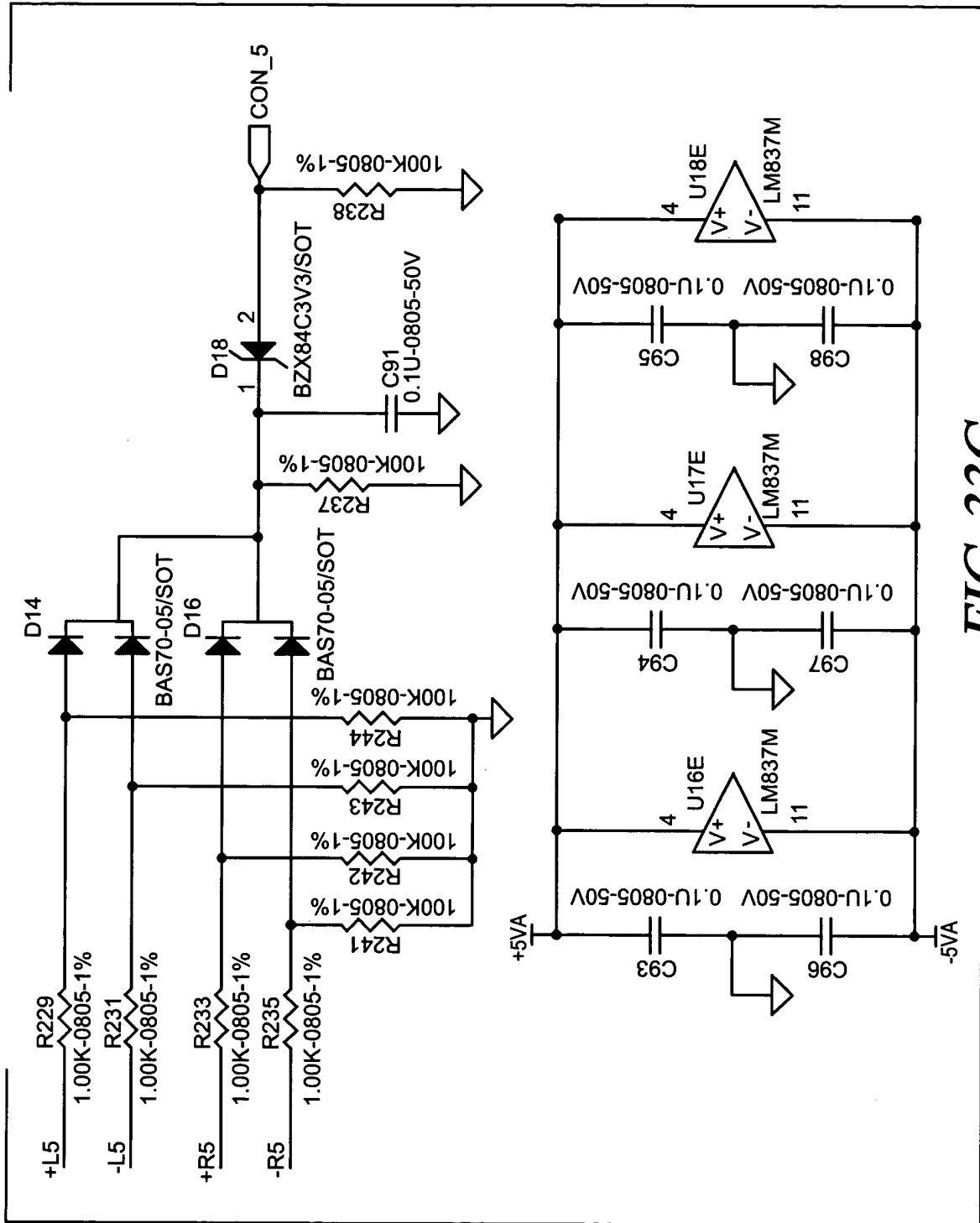


FIG. 33C



FIG. 33D

+

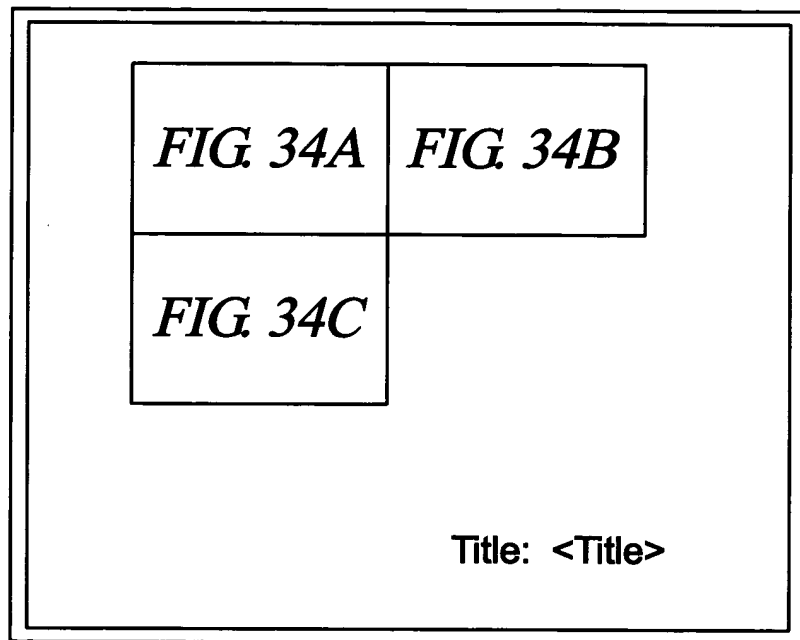


FIG. 34

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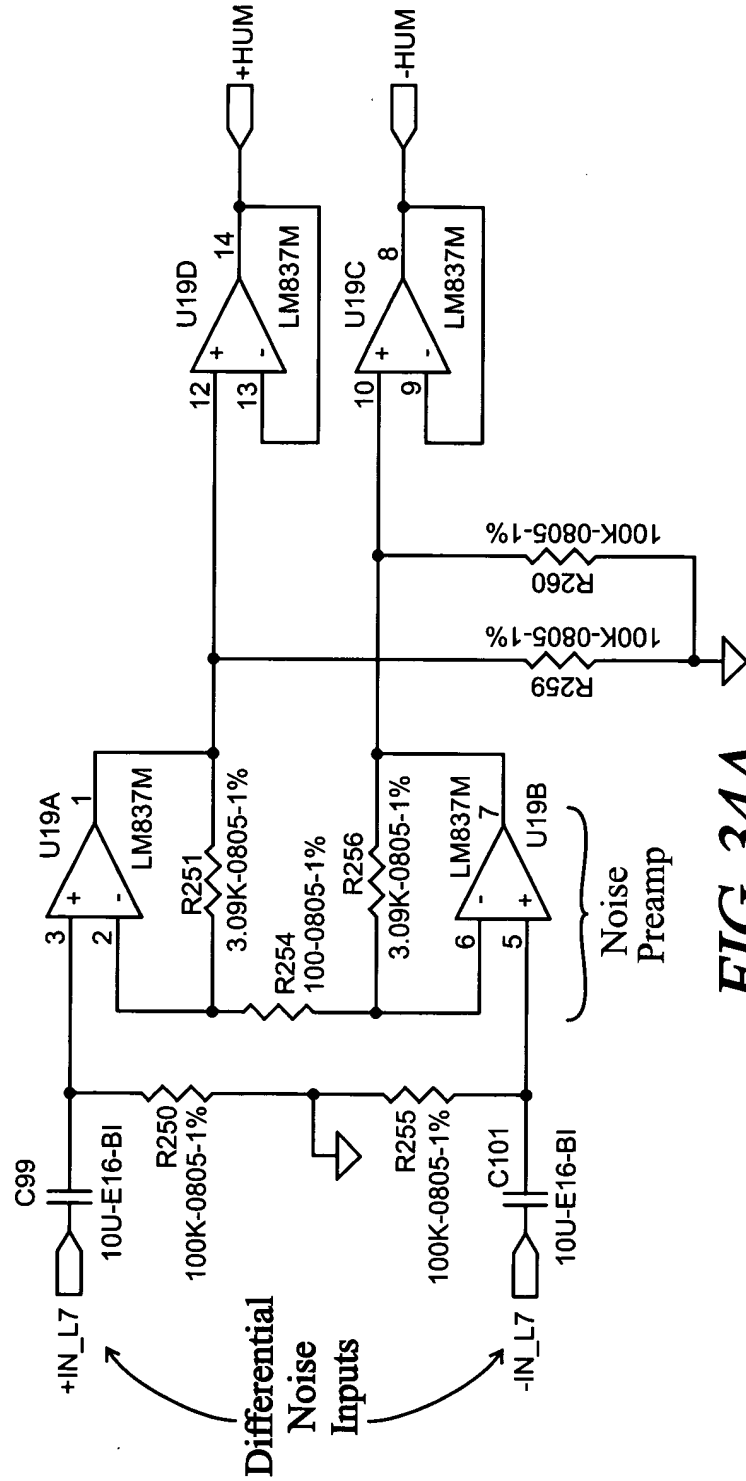
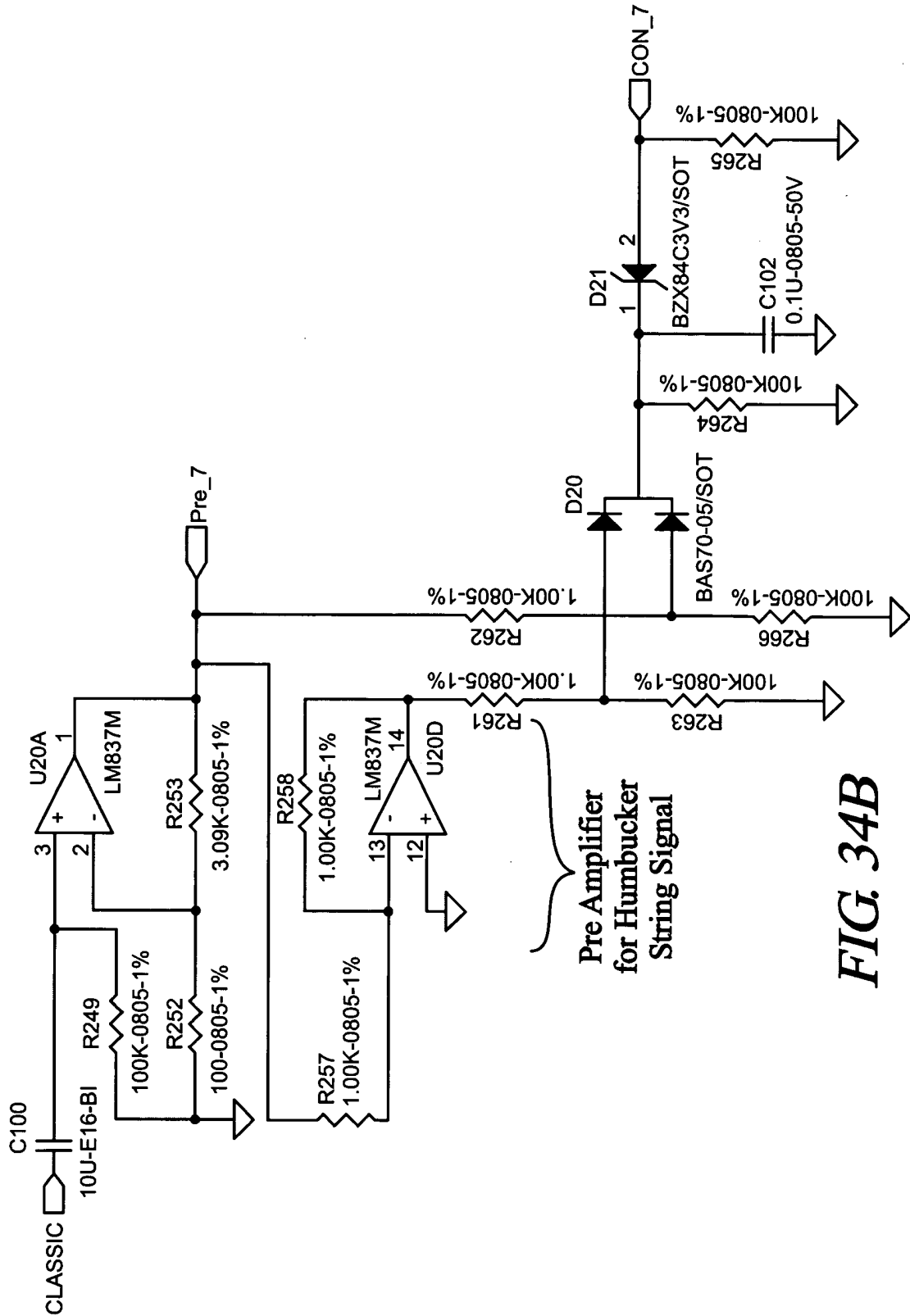


FIG. 34A



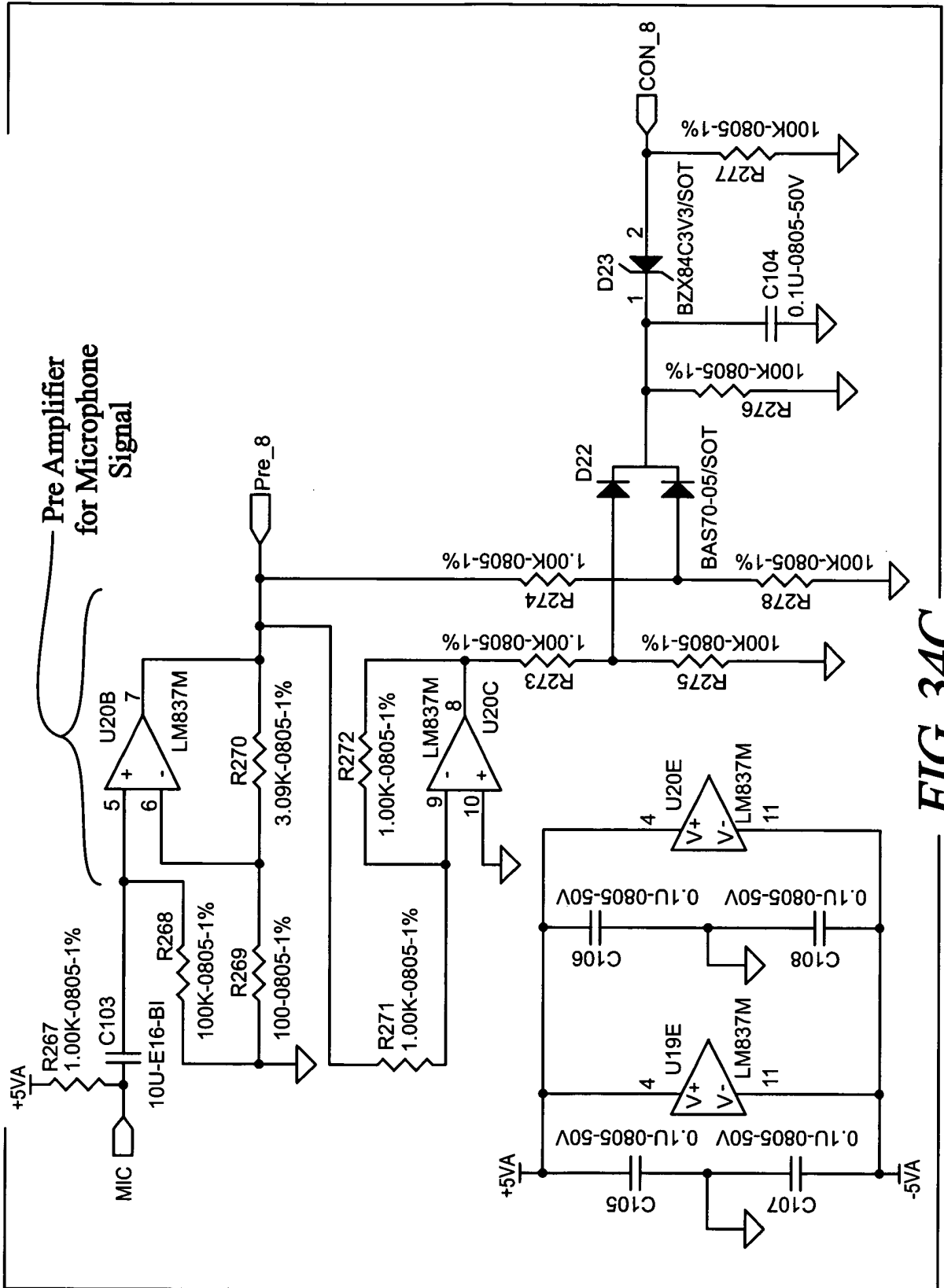


FIG 34C

+

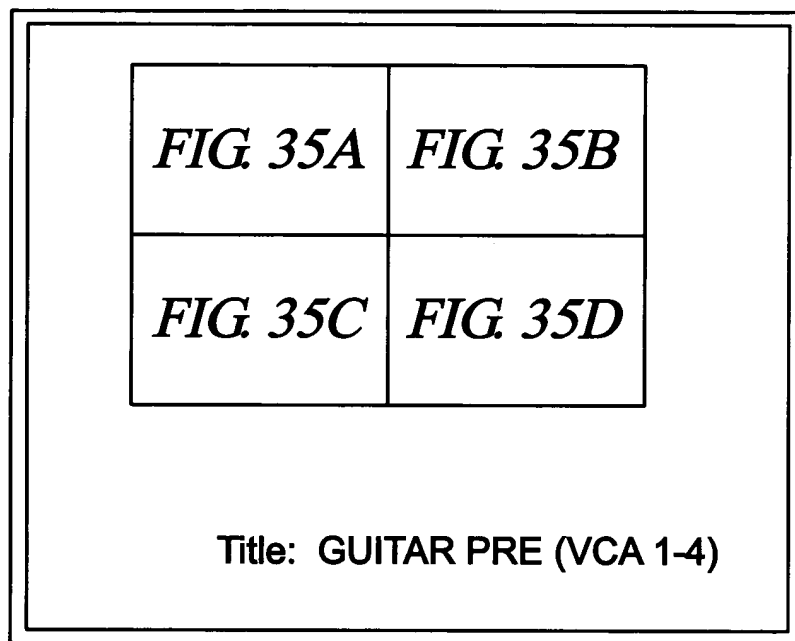
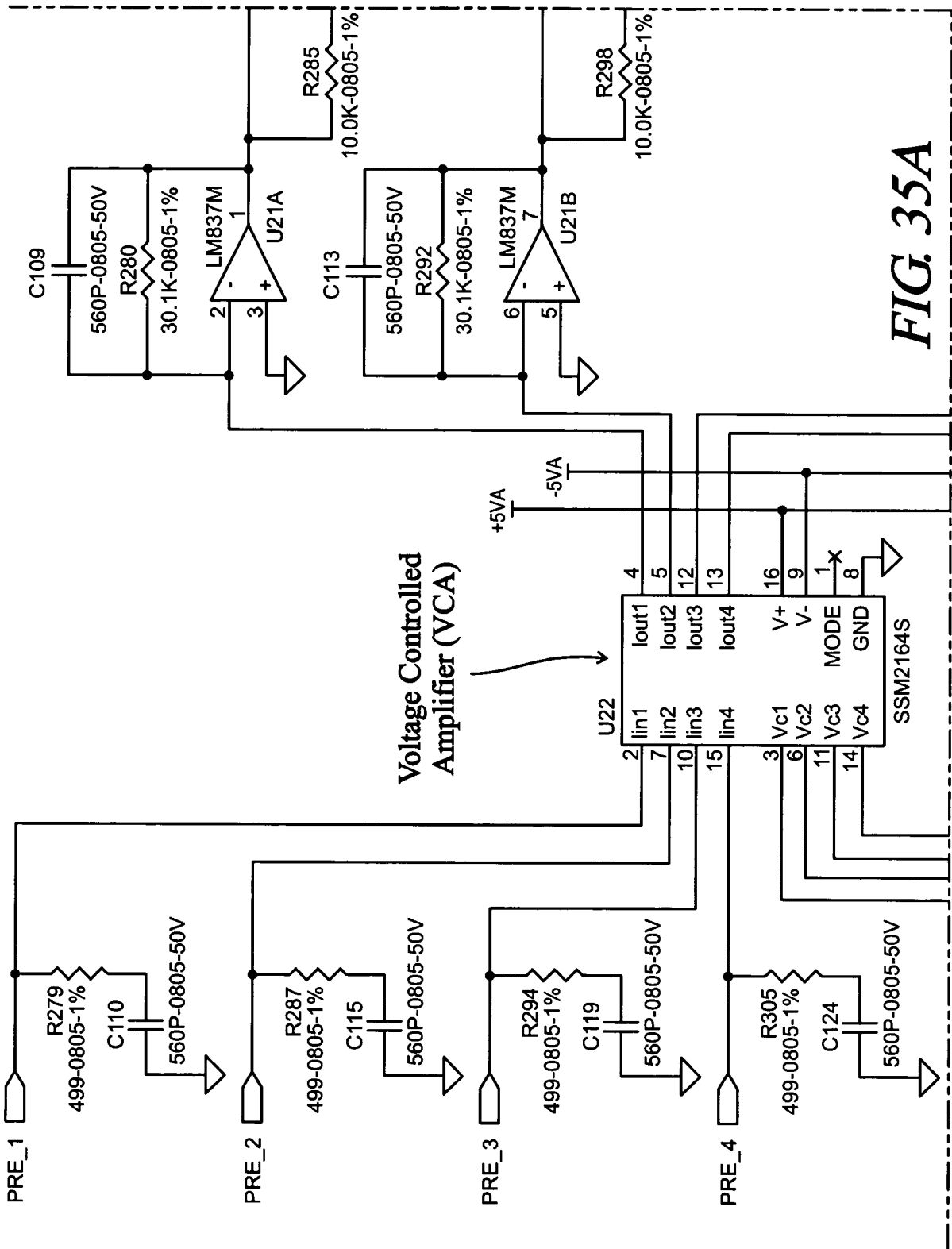


FIG. 35

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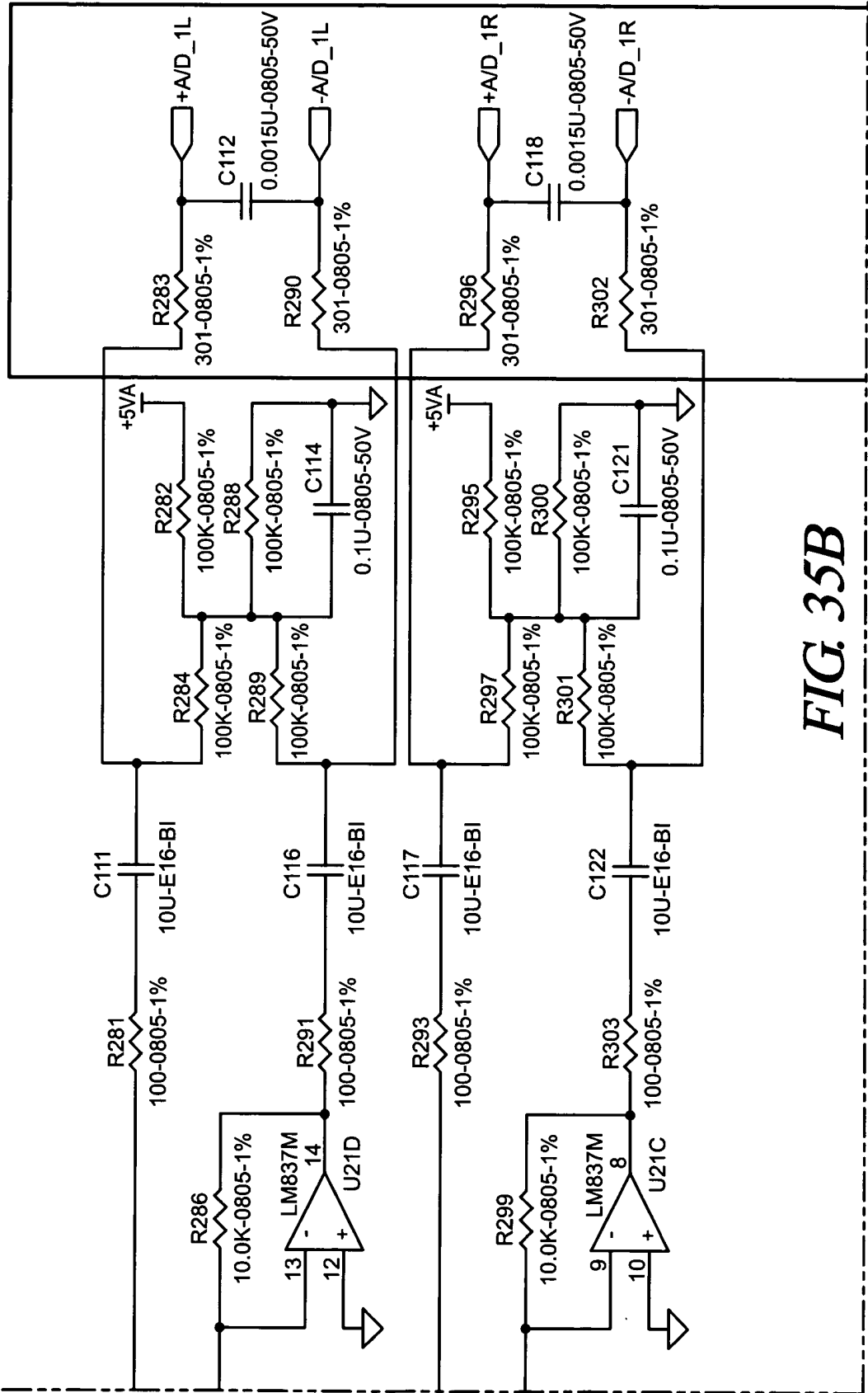


FIG. 35B

+

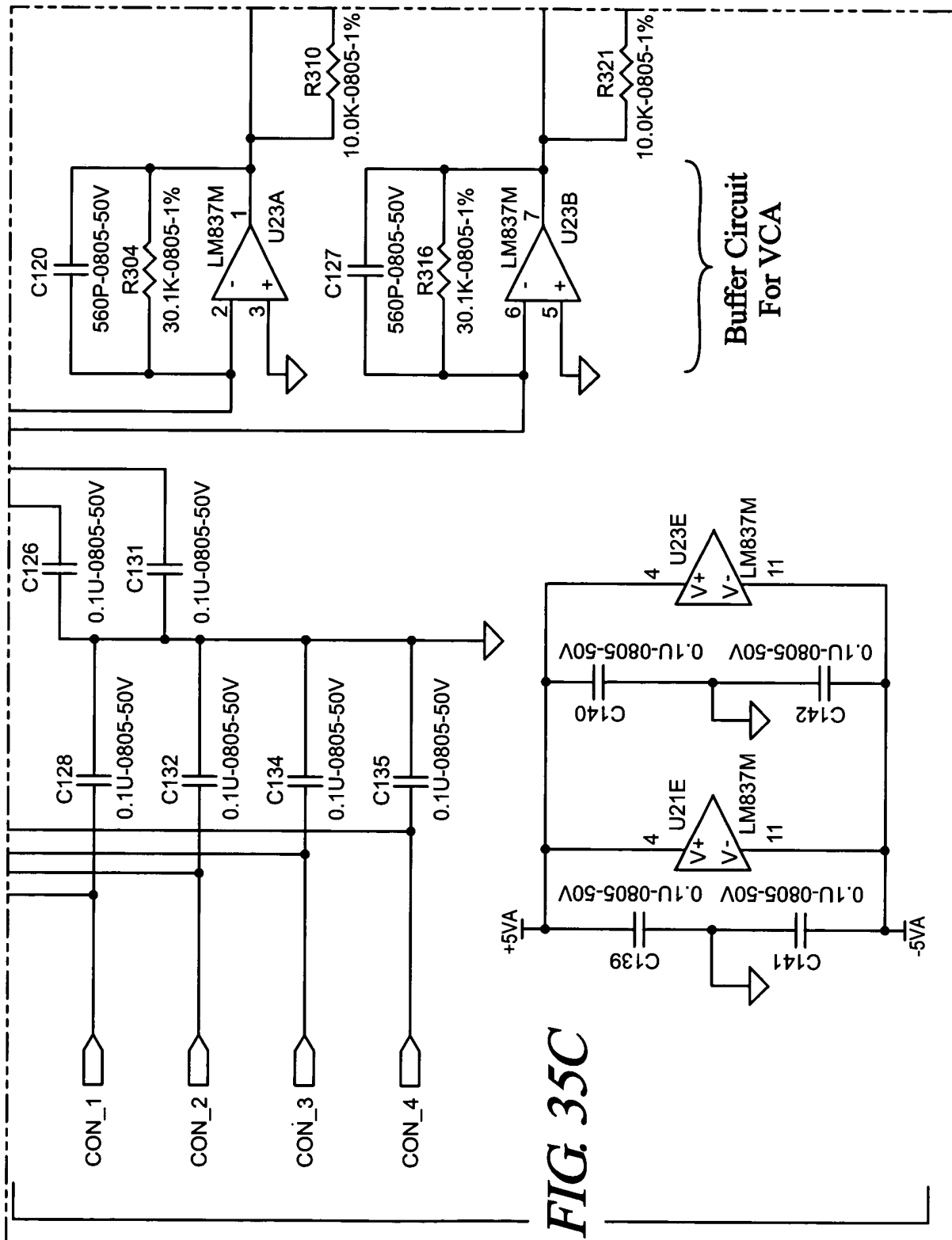


FIG. 35C

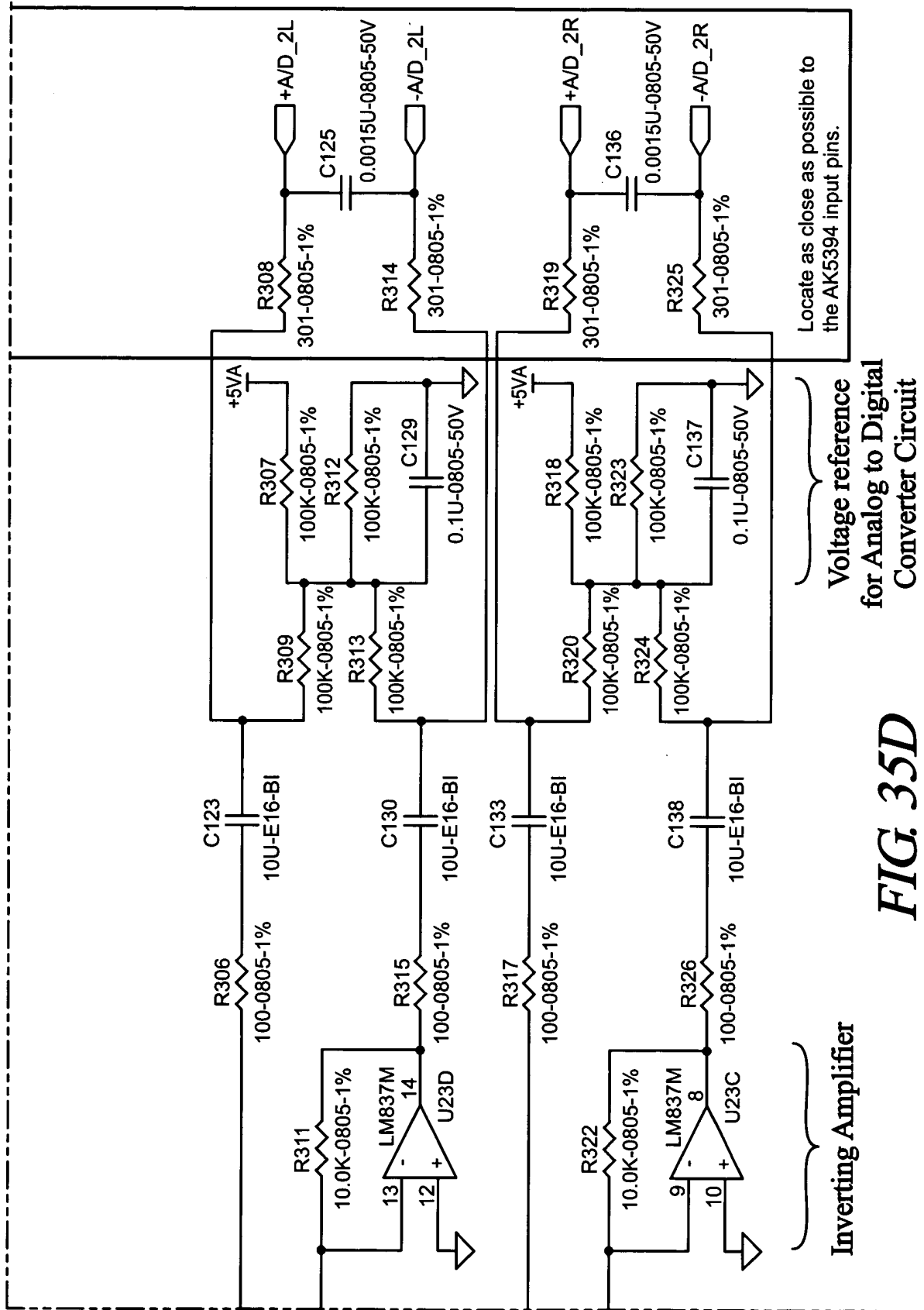


FIG. 35D

Inverting Amplifier

Voltage reference
for Analog to Digital
Converter Circuit

+

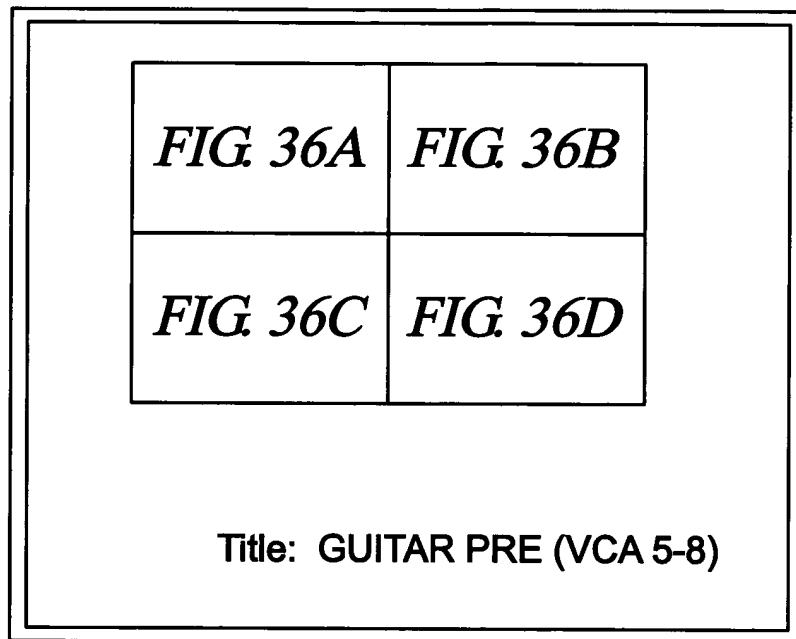


FIG. 36

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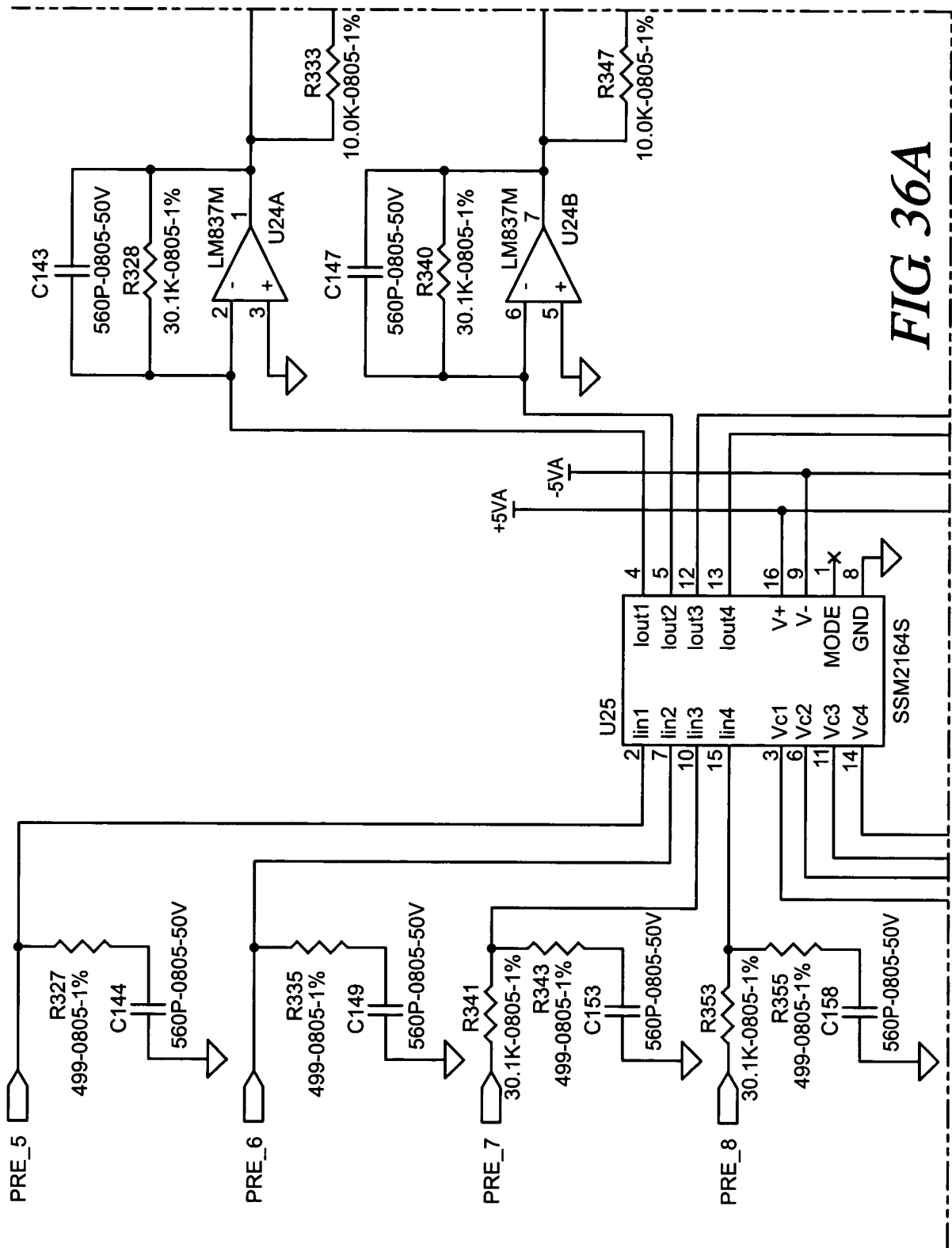




FIG. 36B

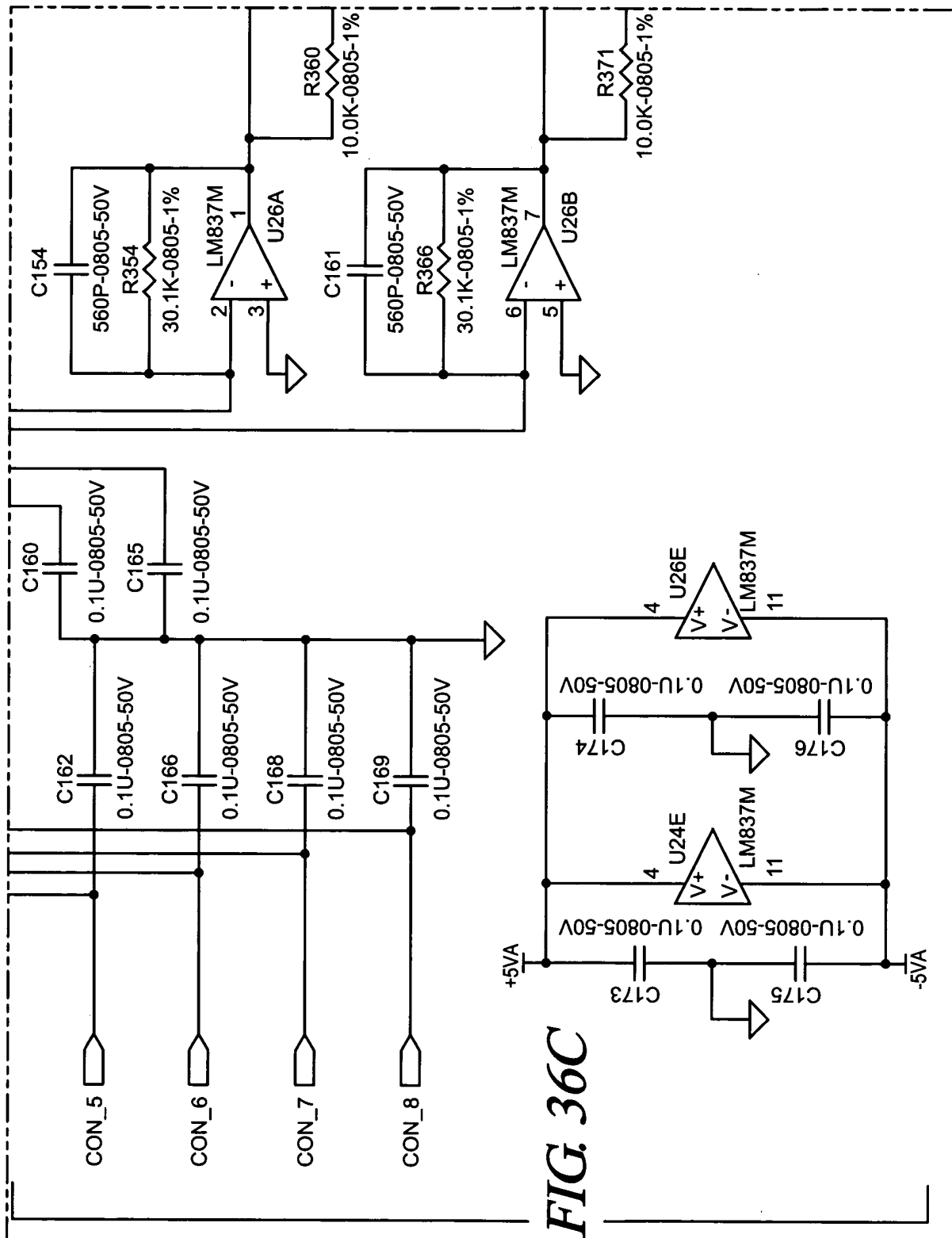


FIG. 36C

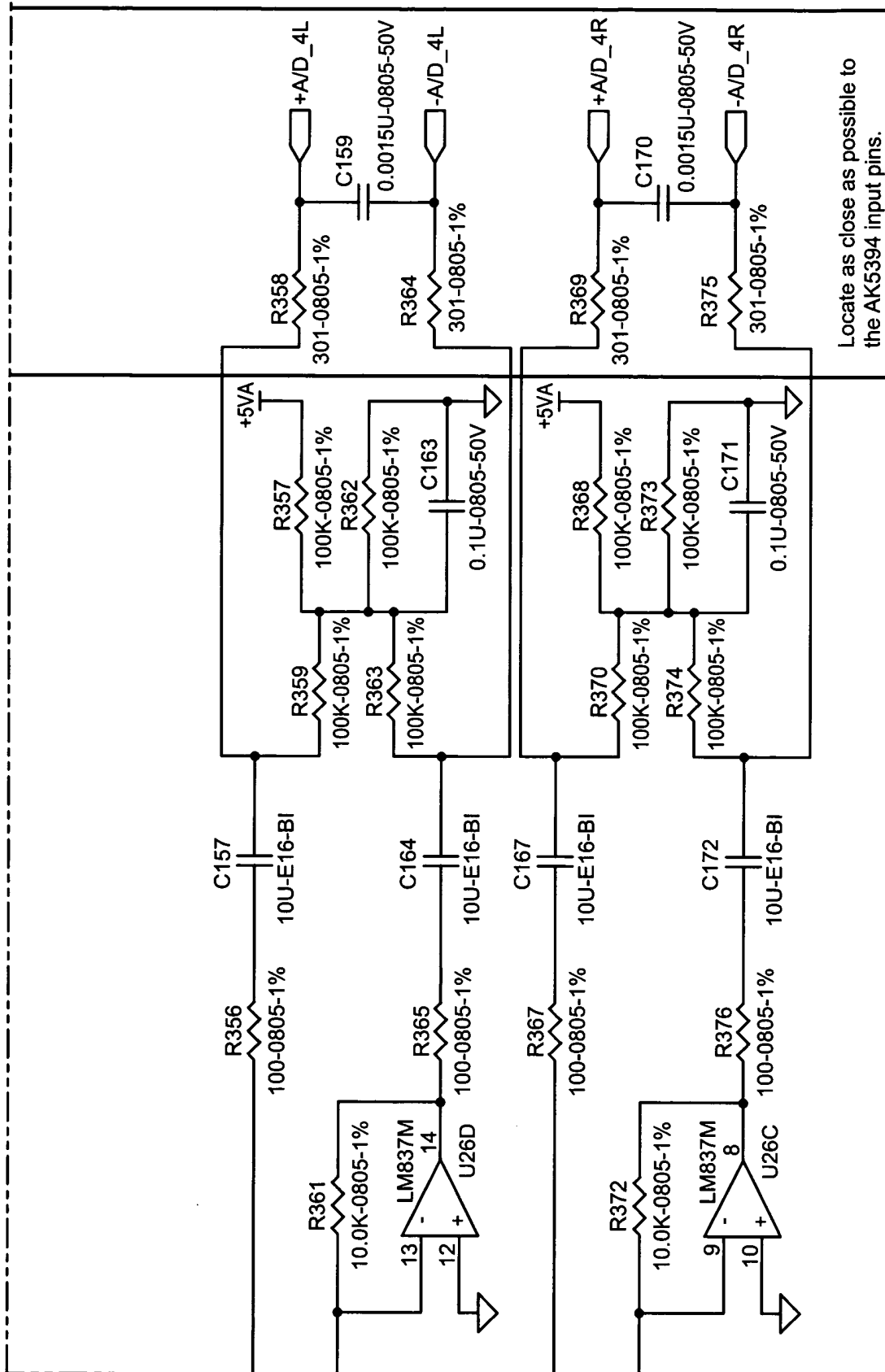


FIG. 36D